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[Name of Document] Specification 1

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[Name of Document] SPECIFICATION

[Title of the Invention] ELECTRO-OPTICAL DEVICE AND
ELECTRONIC APPARATUS

[Claims]

[Claim 1] An electro-optical device including, on a substrate, data lines extending in a first direction, scanning lines extending in a second direction intersecting the data lines, and pixel electrodes and thin film transistors disposed to correspond to intersection regions of the data lines and the scanning lines so as to constitute a part of a laminated structure, the device comprising, on the substrate:

storage capacitors electrically connected to the thin film transistors and the pixel electrodes;

shielding layers disposed between the data lines and the pixel electrodes; and

interlayer insulating films disposed as the base of the pixel electrodes so as to constitute a part of a laminated structure;

wherein contact holes for electrically connecting the thin film transistors and the pixel electrodes are formed in the interlayer insulating films, and the entire inside region of the contact holes is filled with a filler made of a conductive material.

[Claim 2] The electro-optical device according to Claim

1, wherein the surface of the interlayer insulating films is planarized.

[Claim 3] The electro-optical device according to Claim 1 or 2, wherein another contact holes are formed in the interlayer insulating films instead of or in addition to the contact holes and the entire inside region of another contact holes is filled with the filler made of the conductive material.

[Claim 4] The electro-optical device according to any one of Claims 1 to 3, wherein the filler is made of a light-shielding material.

[Claim 5] The electro-optical device according to any one Claims 1 to 4, wherein the filler is made of a transparent conductive material.

[Claim 6] The electro-optical device according to any one of Claims 1 to 5, wherein a coating member is formed on the inner surface of the contact holes and the filler is formed on the coating member.

[Claim 7] The electro-optical device according to Claim 6, wherein the filler is made of a polyimide material instead of the conductive material.

[Claim 8] The electro-optical device according to any one of Claims 1 to 7, wherein the contact holes are formed in light-shielding regions corresponding to positions in which the scanning lines and the data lines are formed.

[Claim 9] The electro-optical device according to any one of Claims 1 to 8, wherein the data lines are formed of the same film as one of a pair of electrodes constituting the storage capacitor.

[Claim 10] The electro-optical device according to Claim 9, wherein the data lines constitute a laminated body of an aluminum film and a conductive polysilicon film.

[Claim 11] The electro-optical device according to any one of Claims 1 to 10, wherein a relay layer for electrically connecting one of the pair of electrodes constituting the storage capacitor to the pixel electrode is included as a part of the laminated structure.

[Claim 12] The electro-optical device according to Claim 2, wherein instead of or after performing a planarization process on the surface of the interlayer insulating film,

the plurality of pixel electrodes is disposed in a plane and comprises a first pixel electrode group inversely driven in a first period and a second pixel electrode group inversely driven in a second period complementary to the first period,

the data lines comprise main line portions which extend above the scanning lines so as to intersect the scanning lines and overhanging portions which overhang from the main line portions along the scanning lines,

a counter electrode facing the plurality of pixel

electrodes are formed on a counter substrate disposed to face the substrate, and

convex portions are formed in regions which are to be gaps between the pixel electrodes adjacent to each other by interposing the scanning lines in plan view due to the presence of the overhanging portions on the base surfaces of the pixel electrodes on the substrate.

[Claim 13] The electro-optical device according to Claim 2, wherein instead of or after performing a planarization process on the surface of the interlayer insulating film,

the plurality of pixel electrodes is disposed in a plane and comprises a first pixel electrode group inversely driven in a first period and a second pixel electrode group inversely driven in a second period complementary to the first period,

a counter electrode facing the plurality of pixel electrodes and convex portions formed in regions which are to be gaps between the pixel electrodes adjacent to each other in plan view are formed on a counter substrate disposed to face the substrate, and

the convex portions have gentle surface step differences caused by removing the planarized films formed on the convex portions by an etching process and causing the surface of the convex portion exposed after removing the planarized films to recede.

[Claim 14] The electro-optical device according to any one of Claims 1 to 13, wherein a dielectric film constituting the storage capacitor comprises a plurality of layers including different materials and also forms a laminated structure comprising a layer made of a material having a higher dielectric constant than the dielectric constants of the other layers.

[Claim 15] An electronic apparatus comprising the electro-optical device according to any one of Claims 1 to 14.

[Claim 16] An electro-optical device including, on a substrate, data lines extending in a first direction, scanning lines extending in a second direction intersecting the data lines, and pixel electrodes and thin film transistors disposed to correspond to intersection regions of the data lines and the scanning lines so as to constitute a part of a laminated structure, the device comprising, on the substrate:

storage capacitors electrically connected to the thin film transistors and the pixel electrodes;

light-shielding films disposed between the data lines and the pixel electrodes; and

interlayer insulating films disposed as the base of the pixel electrodes so as to constitute a part of a laminated structure;

wherein contact holes for electrically connecting the thin film transistors and the pixel electrodes are formed in the interlayer insulating films, and the entire inside region of the contact holes is filled with a filler made of a conductive material.

[Detailed Description of the Invention]

[0001]

[Technical Field of the Invention]

The present invention relates to electro-optical devices such as liquid crystal devices and electronic apparatuses. The present invention also relates to an electrophoresis device such as an electronic paper and the like.

[0002]

[Background Art]

In a conventional art, an electro-optical device such as a liquid crystal device capable of displaying an image, in which an electro-optical material such as liquid crystal is interposed between a pair of substrates and light is transmitted so as to pass through the substrates and the electro-optical material, is disclosed. 'Displaying the image' for example, is realized by changing the state of an electro-optical material in each pixel to change the transmittance ratio of light and by causing light with different gray scale levels to be sensed in each pixel.

[0003]

Such an electro-optical device is active-matrix driven by including, on one of the pair of substrates, pixel electrodes arranged in a matrix, scanning lines and data lines positioned to thread through the pixel electrodes, and TFTs (thin film transistors) as pixel switching elements. According to the electro-optical device capable of being active matrix driven, the TFTs are provided to control conduction between the pixel electrodes and the data lines. The TFTs are electrically connected to the scanning lines and the data lines. Therefore, it is possible to control the turning on and off of the TFTs through the scanning lines and to apply image signals received through the data lines to the pixel electrodes, that is, to change the transmittance ratio of light in each pixel when the TFTs are turned on. For example, Patent Document 1 would be referred to.

[0004]

According to the above-mentioned electro-optical device, the above-mentioned various components are formed on one substrate. However, when the components are expanded two-dimensionally, the components occupy a large area. Therefore, a pixel aperture ratio (that is, a ratio of a region through which light passes to the entire surface of the substrate) may decrease. Therefore, according to the

conventional art, a method of forming the above-mentioned various components three-dimensionally, that is, a method of laminating the various components by interposing interlayer insulating films is adopted. More specifically, the TFTs and the scanning lines functioning as gate electrode films of the TFTs are formed on the substrate. The data lines are formed on the TFTs and the scanning lines. The pixel electrodes are formed on the data lines. According to such a construction, it is possible to improve the pixel aperture ratio by appropriately arranging the various components as well as to miniaturize the electro-optical device.

[0005]

At this time, the interlayer insulating films made of, for example, a silicon oxide film are formed among the above-mentioned various components so as to prevent the generation of electric short circuit among these components. Further, contact holes are formed in predetermined places of the interlayer insulating films because, for example, the drains of the TFTs must be electrically connected to the pixel electrodes and because specific components must be electrically connected to each other. Dry etching the interlayer insulating films commonly forms the contact holes.

[0006]

[Patent Document 1] Japanese Unexamined Patent

Application Publication No. 2002-156652

[0007]

[Problems to be Solved by the Invention]

However, the electro-optical device having such a structure has the following problems. In general, according to the electro-optical device, it is necessary not to disturb the alignment state of the electro-optical material such as the liquid crystal. However, according to the electro-optical device having the above-mentioned laminated structure, it is difficult to meet such a requirement. When the alignment state of the liquid crystal is disturbed, the quality of an image may deteriorate. For example, when an image, in which an entire surface is blacked, is desired to be displayed, light leakage occurs in the disturbed portion, thereby deteriorating contrast. Further, a layer contacting the liquid crystal is preferably as flat as possible in order not to disturb the liquid crystal.

[0008]

Such problems occur due to the following reasons. First, the layer contacting the liquid crystal must be at the uppermost layer in the laminated structure. That is, the uppermost layer is preferably flat in order not to disturb the alignment state of the liquid crystal. However, in case where the laminated structure is formed after the above-mentioned various components are formed, the heights

of the components are different from each other. Therefore, in general, the step differences caused by the heights overlap with each other and affect each other toward upper layers. As a result, a complicated concavo-convex is caused in the uppermost layer. When the concavo-convex is generated, the alignment state of the liquid crystal may be disturbed in response thereto. Therefore, it is difficult to maintain the alignment state as a predetermined state. Furthermore, the deterioration of the flatness due to the above-mentioned reasons becomes more serious in response to the complexity of the structure of the electro-optical device.

[0009]

Second, the contact holes formed in the interlayer insulating films also deteriorate the flatness. For example, when an alignment film is formed in the uppermost layer and also the contact holes are formed under the alignment film in the above-mentioned laminated structure, concave portions corresponding to the positions, where the contact holes are formed, are formed on the alignment film. This is because the contact hole has a hollow inside as noted from the name.

[0010]

On the other hand, 'the light leakage' mentioned as an example of the deterioration of the quality of an image may

occur because the flatness of the laminated structure is not met and the contact holes exist. This is because the contact hole has the hollow inside as mentioned above, thereby easily transmitting light. That is, light passing through the hollow of the contact hole is mixed with the image and then may deteriorate the quality of the image.

[0011]

Accordingly, in order to solve the above-mentioned problems, it is an object of the invention to provide an electro-optical device and an electronic apparatus capable of displaying an image with excellent quality by maintaining the flatness of a laminated structure on a substrate and also preventing light leakage caused by contact holes formed in the laminated structure.

[0012]

[Means for Solving the Problems]

In order to achieve the above-mentioned object, the electro-optical device according to the invention having, on a substrate, data lines extending in a first direction, scanning lines extending in a second direction intersecting the data lines, and pixel electrodes and thin film transistors disposed to correspond to intersection regions of the data lines and the scanning lines so as to constitute a part of a laminated structure includes, on the substrate: storage capacitors electrically connected to the thin film

transistors and the pixel electrodes; shielding layers disposed between the data lines and the pixel electrodes; and interlayer insulating films disposed as the base of the pixel electrodes so as to constitute a part of the laminated structure, in which a dielectric film constituting the storage capacitor includes a laminated body formed of a plurality of layers including different materials, one layer being made of a material having a dielectric constant higher than the dielectric constants of the other layers, contact holes for electrically connecting the thin film transistors to the pixel electrodes are formed in the interlayer insulating films, and the entire inside region of the contact holes is filled with a filler made of a conductive material.

[0013]

According to the electro-optical device of the invention, the electro-optical device can be driven in an active matrix by including the scanning lines, the data lines, the pixel electrodes, and the thin film transistors. Furthermore, in the above electro-optical device, it is possible to achieve miniaturization of the whole apparatus by including the aforementioned various constituents as a part of the laminated structure, and it is possible to improve the pixel aperture ratio by arranging the various constituents appropriately.

[0014]

Moreover, in the invention, other than the aforementioned various constituents, the storage capacitors, the shielding layers, and the interlayer insulating films are provided as the constituents of the laminated structure. First, provision of storage capacitors enables the invention to improve a potential holding property of the pixel electrodes. By doing so, it is possible to display images with high contrast. Second, provision of shielding layers between the data lines and the pixel electrodes enables the invention to prevent the occurrence of capacitance couplings between the data lines and the pixel electrodes in advance. Namely, the electrical conduction of the data lines enables reduction of the probability of the occurrence of potential variation in the pixel electrodes, and thus, it is possible to display images with higher quality.

[0015]

Third, according to the invention, in particular, the contact holes for electrically connecting the thin film transistors to the pixel electrodes are included in the interlayer insulating films under the pixel electrodes. Further, the filler made of the conductive material is included in the entire inside region of the contact holes.

[0016]

Therefore, it is possible to realize excellent electric

connection between the thin film transistors and the pixel electrodes and also to realize excellent and firm electric connection compared with the conventional art due to the presence of the filler. This is because the fillers formed of the conductive material exist in contact portions between the contact holes and the thin film transistors or between the contact holes and the pixel electrodes thereby to increase the contact area of both members in the contact portions and to reduce the resistance value of the contact area.

[0017]

Also, according to the invention, in particular, it is possible to obtain the following effects due to the presence of the filler. That is, due to the presence of the filler, it is possible to prevent the inside of the contact holes from being hollow like in the conventional art. Therefore, a concave portion is not formed in the laminated structure formed on the contact holes. Thus, for example, even in case where the alignment film is formed on the pixel electrodes, the concave portion is not formed in the alignment film and then the alignment state of the liquid crystal touched with the alignment film is not disturbed. As a result, for example, it is possible to prevent the quality of an image from deteriorating due to the reduction of a contrast ratio. Also, as a principle, light that

transmits the hollow like in the conventional art does not exist (this is because 'the hollow' does not exist due to the presence of the filler), and thereby it is possible to prevent the deterioration of the quality of the image.

[0018]

Also, as the specific aspect of the filler, the filler is preferably made of a light-shielding material and a transparent conductive material according to the following various aspects of the invention which will be described later. However, the invention is not limited to the specific aspect of the filler. That is, the inside of the contact holes may be filled with any materials. Therefore, it is possible to use all kinds of metal materials as 'the filler made of the conductive material' according to the invention.

[0019]

In the invention, according to circumstances, the entire inside region of the contact hole formed in an interlayer insulating film other than the interlayer insulating films arranged as the base of the pixel electrodes (that is, an interlayer insulating film positioned in a deeper portion) may be filled with the filler. According to the above-mentioned structure, it is still possible to prevent the generation of the concave portion on the alignment film.

[0020]

Furthermore, 'the electric connection' according to the invention includes a case where the thin film transistor is directly 'connected' to the pixel electrode through the contact hole as well as a case where another element such as a storage capacitor is interposed therebetween and then those are 'connected' through the contact hole. In this case, it is assumed that the contact holes exist between the thin film transistor and the storage capacitor and between the storage capacitor and the pixel electrode. However in this case, 'the contact hole' according to the invention means the latter contact hole because the contact hole is formed in 'the interlayer insulating film arranged as the base of the pixel electrode.'

[0021]

According to one aspect of the electro-optical device of the invention, the surface of the interlayer insulating films is planarized.

[0022]

According to this aspect, the surface of the interlayer insulating films is planarized by the planarization process. Therefore, it is possible to prevent the generation of the step difference and the concave portion in the pixel electrode and the alignment film. Furthermore, it is possible to improve such an effect in that complicated

concavo-convex shape is formed on the surface of the uppermost interlayer insulating film because the electro-optical device according to the invention includes the scanning lines, the data lines, the thin film transistors, the storage capacitor, and the shielding layer and thus, the structure thereof is further complicated. According to the invention, in particular, because the inside of the contact hole is filled with the filler, and then the filler protrudes the surface of the interlayer insulating films and a convex portion is formed instead of the concave portion generated in the conventional art. However, according to the present aspect, although the protrusion and the convex portion exist, it is possible to planarize them.

[0023]

Furthermore, in particular, for example, a CMP (chemical mechanical polishing) process and an etchback process are used for 'the planarization process' according to the invention. However, other various planarization processes may be used.

[0024]

In the CMP process, in general, the surface of a coating process substrate is abraded by mechanical and chemical processes by connecting the surface of the coating process substrate to the surface of an abrasive cloth (pad) while rotating the coating process substrate and the

abrasive cloth (pad) and also by supplying an abrasive solution (slurry) including silica particles to the connection portion. As a result, the surface of the coating process substrate is planarized.

[0025]

In the etchback process, after forming a flat film such as a photo-resist film and a SOG (spin on glass) film on the uneven surface as a sacrificial film, the sacrificial film is etched to the concavo-convex surface (thus, the uneven surface is 'planarized'). The etchback process is a technology for planarizing the surface. However, in the invention, the sacrificial film is not essential. For example, after excessively forming a film made of the filler to the surface of the interlayer insulating films to an extent of more than filling the space inside the contact hole (that is, to overflow from the contact hole), the excessively formed portion is completely etched in a region excluding the contact hole. Therefore, the filler exists only inside the contact hole and the flat surface is formed.

[0026]

In addition, in accordance with the construction that the surface of the interlayer insulating films is planarized according to the invention, in a case where every scanning line or every row of the pixel electrodes which are connected to the scanning lines is driven by the different

polarities (that is, the '1H inversion driving'; see the later description), the transverse electric field may occur between the adjacent pixel electrodes. Thus, the alignment state of the liquid crystal may be disturbed. In this point, as described below, although means for suppressing the occurrence of the transverse electric field by convex portions being provided to the surface of the interlayer insulating films is preferably employed and the other means may be preferably employed as follows.

[0027]

Namely, the inversion of the polarity may be performed not for every scanning line but for every one field period (one vertical scanning period). In other words, the '1V inversion driving' is performed. By doing so, since the adjacent pixel electrodes are not driven by the different polarities during any field period, the transverse electric field cannot occur in principle.

[0028]

However, when the 1V inversion driving is employed, the following problems occur. Namely, one of the difficult problems is that the flicker may occur on the image for every one vertical scanning period at the time of inversion of the polarity.

[0029]

Therefore, in these cases, as described later in

details in embodiments, the multiplication-speed field inversion driving is preferable. Herein, the multiplication-speed field inversion driving is a driving manner in which the one field period is shorten by half in comparison to the conventional period (for example, assuming that the conventional period is driven at 120 [Hz], the 'half' is preferably 1/60 [s] or less). Therefore, in a case of the 1V inversion driving, the period of the inversion of the polarity is a half of the conventional period. By doing so, the one vertical scanning period is shortened. In other words, the changeover between the screen corresponding to the positive polarity and the screen corresponding to the negative polarity is further rapidly carried out, so that the aforementioned flicker cannot be perceived by human beings' eyes.

[0030]

Like this, in accordance with the multiplication-speed field inversion driving manner, it is possible to display images with higher quality without flicker.

[0031]

According to another aspect of the electro-optical device of the invention, another contact holes instead of or in addition to the contact holes may be formed in the interlayer insulating films. Therefore, the entire inside region of another contact holes is filled with the filler

made of the conductive material.

[0032]

According to such aspect, another contact holes is formed in the interlayer insulating films which is disposed as the base of the pixel electrodes. Therefore, the entire inside region of another contact holes is filled with the filler made of the conductive material. Here, as another contact hole, for example, the contact hole is one for electrically connecting the storage capacitor to the shielding layer.

[0033]

The above-mentioned effect can be surely obtained because a part or the entire part of the contact holes formed in the interlayer insulating films is filled with the filler.

[0034]

According to another aspect of the electro-optical device of the invention, the filler is made of the light-shielding material.

[0035]

According to such aspect, because the filler is made of the light-shielding material, it is possible to prevent the light leakage caused by the presence of the contact hole. That is, because the progress of light is prevented by the filler, it is possible to prevent light that transmits the

contact hole whose inside is hollow from being incident on an image like in the conventional art. Therefore, it is possible to prevent useless light from being incident on an image and thereby to display an image with more excellent quality than ever.

[0036]

According to the present aspect, because the filler intercepts light, it is possible to prevent light from being incident on the semiconductor layer that forms the thin film transistor, in particular, the channel region. Therefore, it is possible to prevent the generation of light leakage current and thereby to display an image with excellent quality without flicker on the image.

[0037]

The specific examples of 'the light-shielding material' according to the present aspect include a metal monomer including at least one among Ti (Titanium), Cr (Chrome), W (Tungsten), Ta (Tantalum), and Mo (Molybdenum), an alloy, metal silicide, poly silicide, and a laminated material formed of the above materials.

[0038]

According to another aspect of the electro-optical device of the invention, the filler is made of a transparent conductive material.

[0039]

According to such aspect, it is possible to form the filler made of the same material as that of the pixel electrode. This is because the pixel electrode is generally made of the transparent conductive material such as ITO (Indium Tin Oxide) and IZO (Indium Zinc Oxide). Therefore, according to the present aspect, it is possible to perform a process of forming or film forming the pixel electrode and a process of filling the inside of the contact hole with the filler at the same time and thereby to reduce the manufacturing cost of the pixel electrode and the filler.

[0040]

In this case, because the length of the contact hole is generally larger than the thickness of the pixel electrode disposed as a part of the uppermost layer, although the filler is made of the transparent conductive material, the filler is expected to have a light-shielding effect (that is, the thicker the filler is, the smaller transparency is and thus it is difficult for light to transmit the filler). Therefore, according to the present aspect, it is possible to obtain the effect of preventing light leakage in the contact hole although the effect of preventing light leakage may be weaker than that obtained when the filler is made of the light-shielding material.

[0041]

According to another aspect of the electro-optical

device of the invention, a coating member is formed in the inner surface of the contact hole. Therefore, the filler is formed on the coating member.

[0042]

According to such aspect, 'a two-layer structure', that is, the coating member and the filler (a structure of 'an inner layer (the filler)' and 'an outer layer (the coating member)') is formed inside of the contact hole. Therefore, for example, it is possible to use a material with higher conductivity for the coating member and to use a material with a higher light-shielding performance for the filler. As a result, it is possible to obtain the above-mentioned various effects. Also, it is possible to adjust the revelation of the above-mentioned various effects by appropriately deciding which effects are to be considered as important among the various effects (for example, the light-shielding effect).

[0043]

According to such aspect, in particular, the filler is preferably made of a polyimide material instead of the conductive material.

[0044]

According to above composition, it is possible to simplify manufacturing processes by forming the filler of the conductive material at the same time the alignment film

is made of the polyimide material on the pixel electrode, that is, to perform the process of forming the alignment film and the process of forming the filler at the same time. As a result, it is possible to reduce the manufacturing cost of the alignment film and the filler.

[0045]

Also, according to the present aspect, unlike the above-mentioned aspect, the filler is not made of the conductive material. However, it is possible to realize electric connection between the thin film transistors and the pixel electrodes as long as the coating member according to the present aspect is made of the conductive material. In this case, the filler is not necessarily made of the conductive material. To be specific, according to the present aspect, the filler is made of the polyimide material. However, the filler may be filled of an insulating material such as an oxide and a nitride instead of the polyimide material.

[0046]

According to another aspect of the electro-optical device of the invention, the contact hole is formed in light-shielding regions corresponding to the positions where the scanning lines and the data lines are formed.

[0047]

According to such aspect, because the contact holes are

formed in the light-shielding regions, it is possible to improve an aperture ratio. Also, because a light-shielding film as well as the scanning lines and the data lines can be formed in the light-shielding regions, it is possible to reduce the amount of light that reaches the contact hole. Therefore, according to the present aspect, it is possible to prevent the light leakage caused by the contact hole and to obtain the various effects of the filler according to the invention. As a result, it is possible to display an image with excellent quality.

[0048]

Another aspect of the electro-optical device of the invention provides the electro-optical device according to Claim 1 or 2, wherein the data lines are formed of the same film as one of a pair of electrodes constituting the storage capacitors.

[0049]

According to the above aspect, the data lines and one electrode in a pair of electrodes which constitute the storage capacitors are made of the same film, in other words, formed on the same layer, or formed in the same manufacturing process. By doing so, it is not necessary to provide both of the data lines and one electrode on separate layers and to take means for separating them by interlayer insulating films. Thus, it is possible to avoid a highly

laminated structure. At this point, the invention is advantageous in that the laminated structure includes the shielding layers formed between the data lines and the pixel electrodes, and thus, an increase in altitude of the laminated structure is expected. The reason for such an advantage is that, excessively multi-layered structure could obstruct easy manufacturing or high manufacturing yield. In addition, even though the data lines and the one electrode of a pair of the electrodes are formed at the same time, by an appropriate patterning process on the aforementioned films, it is possible to facilitate the insulation between them, and no particular problem occurs with respect to this point.

[0050]

In addition, conversely, as apparent from the description of the aspect, in the invention, it is not always necessary to form the data lines and the one electrode of a pair of electrodes which constitute the storage capacitor as the same film. In other words, they may be separately formed in the different layers.

[0051]

According to the above aspect, it is preferable that the data lines include a laminated body of an aluminum film and a conductive polysilicon film.

[0052]

According to the above configuration, it is possible to implement electrical connection between the data lines and the thin film transistors and contact between the conductive polysilicon film that constitute the data lines and the semiconductor layers which constitute the thin film transistors, so that a good electrical connection between them can be obtained.

[0053]

In another aspect of the electro-optical device of the invention, the electro-optical device further includes relay layers, as parts of a laminated structure, for electrically connecting the pixel electrodes to one of a pair of electrodes that constitute the storage capacitors.

[0054]

According to the above aspect, the pixel electrodes and the one of a pair of electrodes of the storage capacitors which respectively constitute a part of the laminated structure are electrically connected through the relay layer that constitutes a part of the same laminated structure. By doing so, for example, the relay layers according to the invention are formed of a two-layered structure, while the upper layer is made of a material which is very compatible with ITO (Indium Tin Oxide) as an example of a transparent conductive material which is generally used as a material of the pixel electrode and the lower layer is made of a

material which is compatible with one electrode of a pair of electrodes which constitute the storage capacitor. Like this, since the relay layer can employ such a flexible construction, it is possible to further suitably implement the application of voltage to the pixel electrodes or the potential holding in the pixel electrodes. Further, in this case, 'contact holes' according to the invention described above are for connecting the relay layer to the pixel electrodes.

[0055]

In another aspect of the electro-optical device of the invention, instead of or after performing a planarization process on the surface of the interlayer insulating film, a plurality of the pixel electrodes is arranged in a plane and at the same time includes a first pixel electrode group which is inversely driven at a first period and a second pixel electrode group which is inversely driven at a second period which is complementary to the first period; and the data lines include main line portions which are extended to upper sides of the scanning lines so as to intersect the scanning lines and overhanging portions which overhang from the main line portions along the scanning lines; the electro-optical device further includes a counter electrode which faces the plurality of the pixel electrodes on a counter substrate which is provided opposite to the

substrate; and convex portions are formed in the regions to be gaps between the pixel electrodes, which are adjacent to each other by interposing the scanning lines in plan view corresponding to the overhanging portions, on the base surfaces of the pixel electrodes on the substrate.

[0056]

According to the above aspect, a plurality of pixel electrodes is arranged on the first substrate in a plane and includes a first pixel electrode group which is inversely driven at the first period and a second pixel electrode group which is inversely driven at the second period which is complementary to the first period. Both types of pixel electrodes exist; (i) pixel electrodes which are driven by the driving voltages with opposite polarities every time during the inversion driving and are adjacent to each other, and (ii) pixel electrodes which are driven by the driving voltages with the same polarities every time during the inversion driving and are adjacent to each other. Both of the types exist in case of the electro-optical device such as matrix driven liquid crystal apparatus that employs, for example, the aforementioned 1H inversion driving manner. Therefore, the transverse electric field is generated between the adjacent pixel electrodes that belong to the different pixel electrode groups (that is, the adjacent pixel electrodes to which the potentials of opposite

polarities are applied).

[0057]

In the invention, particularly, the data lines include the overhanging portions along the scanning lines from the main line portions which extend above the scanning lines to intersect with the scanning lines. In addition, at the presence of the overhanging portions, convex portions are formed in the regions to be gaps between the pixel electrodes which are adjacent by interposing the scanning lines in plan view on the base surface of the pixel electrodes. Namely, the base surfaces of the pixel electrodes become surfaces on which convex portions having constructively predetermined height and predetermined shape are formed.

[0058]

As a result, first, when the edge portions of each pixel electrode are formed to be located on the convex portions, it is possible to relatively strengthen the longitudinal electric field generated between each pixel electrode and each counter electrode in comparison with the transverse electric field generated between the adjacent pixel electrodes (particularly, between the pixel electrodes which belong to the different pixel electrode groups). In other words, since in general, the electric field is getting stronger as the distance between the electrodes is getting

shorter, the edge portion of the pixel electrodes is closer to the counter electrode by the distance corresponding to the height of the convex portions, so that the longitudinal electric field generated between the electrodes can be strengthened. Second, irrespective of whether the edge portions of each pixel electrode on the convex portions are located or not, the transverse electric field generated between the adjacent pixel electrodes (particularly, between the pixel electrodes which belong to the different pixel electrode groups) becomes smaller by the presence of the convex portions in accordance with the dielectric constant of the convex portions, and also by reduction in the volume of the electro-optical materials which the transverse electric field passes through (by the partial substitution thereof at the convex portions), so that it is possible to reduce the influence of the transverse electric field to the electro-optical materials. Therefore, it is possible to reduce operational failure of the electro-optical materials such as the alignment failure of liquid crystal due to the transverse electric field in accordance with the inversion driving manner. At this time, as described above, the edge portions of the pixel electrodes may be located on the convex portions; otherwise, the edge portions of the pixel electrodes may be located in the middle of the slanted or substantially surface of the vertical side of the convex

portions.

[0059]

Furthermore, it is possible to control the height or shape of the convex portions further accurately in comparison with a technique for controlling the height of the edge of the pixel electrodes (in the technique, since slight discrepancies of patterns in plural films are combined, it is basically difficult to control the height or shape of concavo-convex on the uppermost layer that is finally formed, as the desired design) by using the presence of the other wiring lines or elements which are located at the lower portion of the data lines. For the reason, since the operational failure of the electro-optical materials such as alignment failure of the liquid crystal due to the transverse electric field is surely reduced finally, it is possible to improve reliability of the apparatus.

[0060]

In addition, since the light-shielding film for covering positions of operational failure in the electro-optical materials can be formed in a small size, it is possible to increase the aperture ratio of each pixel without image defects such as a light leakage.

[0061]

As a result, since operational failures due to the transverse electric field in the electro-optical materials

such as liquid crystal, etc., can be surely reduced by the formation of the convex portions along the overhanging portions of the data lines, it is possible to relatively easily manufacture the electro-optical device such as liquid crystal apparatuses with high contrast, high brightness, and high quality.

[0062]

In addition, the invention can be adapted to various types of electro-optical devices besides the projection type and reflection type ones.

[0063]

In another aspect of the electro-optical device of the invention, instead of or after performing a planarization process on the surface of the interlayer insulating film, a plurality of the pixel electrodes is arranged in a plane and includes a first pixel electrode group which is inversely driven at a first period and a second pixel electrode group which is inversely driven at a second period which is complementary to the first period; the electro-optical device further includes a counter electrode which faces the plurality of the pixel electrodes on a counter substrate which is provided opposite to the substrate; and convex portions formed in regions to be gaps between the pixel electrodes which are adjacent in plan view and the convex portions have gentle step differences which are formed by

removing the planarization films which are formed in advance on the convex portions by an etching process and retreating the surfaces of the convex portions which are exposed after removing the planarization films.

[0064]

According to the above aspect, although the transverse electric field is generated between the adjacent pixel electrodes belonging to the different pixel electrode groups (that is, the adjacent pixel electrodes to which potentials of opposite polarities are applied), since the convex portions are constructively formed by the etching process for the edge portions of the pixel electrodes which are located at non-opening regions of each pixel or the edge portions of the adjacent pixel electrodes, first, if the edge portions of each pixel electrode are formed to be located on the convex portions, it is possible to relatively strengthen the longitudinal electric field generated between each pixel electrode and each counter electrode in comparison with the transverse electric field generated between the adjacent pixel electrodes. Second, irrespective of whether the edge portions of each pixel electrode on the convex portions are located or not, the transverse electric field generated between the adjacent pixel electrodes becomes smaller by the presence of the convex portions in accordance with the dielectric constant of the convex

portions, and also by reduction in the volume of the electro-optical materials which the transverse electric field passes through, so that it is possible to reduce the influence of the transverse electric field to the electro-optical materials. Therefore, it is possible to reduce operational failure of the electro-optical materials such as the alignment failure of liquid crystal due to the transverse electric field in accordance with the inversion driving manner. At this time, as described above, the edge portions of the pixel electrodes may be located on the convex portions, or not; otherwise, the edge portions of the pixel electrodes may be located in the middle of the slanted or substantially surface of the vertical side of the convex portions.

[0065]

In addition, since the light-shielding film for covering the positions of operational failure in the electro-optical materials can be formed in a small size, it is possible to increase the aperture ratio of each pixel without image defects such as a light leakage.

[0066]

Moreover, in the invention, particularly, since the convex portions having gentle step differences are formed, it is possible to effectively prevent in advance the occurrence of the operational failures of the electro-

optical materials due to the step differences in the vicinity of the convex portions such as the alignment failure of the liquid crystal. In particular, in a case wherein the rubbing process is performed on the alignment films which are formed on the pixel electrodes, when the step differences of the convex portions are gentle, the rubbing process can be relatively easily performed without any non-uniformity, so that is possible to effectively prevent in advance the operational failures of the electro-optical materials such as the alignment failure of the liquid crystal.

[0067]

As a result, it is possible to surely reduce the operational failure due to the transverse electric field in the electro-optical materials such as liquid crystal by the formation of the convex portion, and also, it is possible to suppress the occurrence of the operational failure due to the step differences in the electro-optical materials such as liquid crystal by the gentle step differences, so that it is possible to implement electro-optical device such as liquid crystal apparatus capable of displaying images with high contrast, high brightness, and high quality.

[0068]

According to another aspect of the electro-optical device of the invention, the dielectric film that

constitutes the storage capacitor includes a laminated body formed of a plurality of layers including different materials, in which one layer is made of a material having higher dielectric constant than the dielectric constants of the other layers.

[0069]

According to the above-mentioned aspect, the dielectric film that constitutes the storage capacitor includes a laminated body formed of a plurality of layers including different materials, in which one layer is made of a material having higher dielectric constant than the dielectric constants of the other layers. Therefore, the storage capacitor according to the invention has an excellent property of accumulating charges compared with the conventional art. As a result, it is possible to improve the potential holding property of the pixel electrode and thereby to display an image with excellent quality.

[0070]

Further, 'the high dielectric material' according to the invention includes an insulating material made of at least one among TaOx (tantalum oxide), BST (barium strontium titanate), PZT (zirconate titanate), TiO₂ (Titanium oxide), ZrO₂ (zirconium oxide), HfO₂ (hafnium oxide), SiON (silicon oxynitride), and SiN (silicon nitride). In particular, it is possible to increase the capacitance in a limited region

on a substrate by using the materials having high dielectric constant such as TaOx, BST, PZT, TiO₂, ZrO₂, and HfO₂. It is possible to reduce the amount of stress in the interlayer insulating films by using materials including silicon such as SiO₂ (silicon oxide), SiON (silicon oxynitride), and SiN.

[0071]

According to the various aspects of the invention mentioned above, it is possible to freely combine one aspect with another aspect (However, in some cases, the aspects may not be combined with each other). For example, the aspect where the entire inside region of the contact holes is filled with the filler may be combined with the aspect where the convex portion is formed on the interlayer insulating films. Of course, the electro-optical device may be constructed by combining three or more aspects.

[0072]

In order to achieve the aforementioned object, the electronic apparatus of the invention includes the aforementioned electro-optical device of the invention (moreover, various aspects are included).

[0073]

According to the electronic apparatus of the invention, since the electronic apparatus includes the electro-optical device of the invention, it is possible to implement various types of electronic apparatuses capable of displaying images

with high quality such as a projection type display apparatus (liquid crystal projector), a liquid crystal television set, a mobile phone, an electronic diary, a word processor, a viewfinder type or monitor-direct-viewing type video tape recorder, a workstation, a television telephone, a POS terminal, and a touch panel without the deterioration of the quality of the image such as the deterioration of contrast, which is caused by the contact hole.

[0074]

These functions and the other advantages of the invention will be clarified by the embodiments described below.

[0075]

[Embodiments]

Hereinafter the embodiments of the invention will be described with reference to the drawings. The following embodiment is an electro-optical device of the invention, which is applied to a liquid crystal apparatus.

[0076]

(Constitution of Pixel Portion)

First, the constitution of a pixel portion of the electro-optical device in the embodiment of the invention will be described with reference to Figs. 1 to 4. Herein, Fig. 1 is an equivalent circuit of various elements and wiring lines that are provided in a plurality of pixels in a

matrix which constitutes image display regions in the electro-optical device. Fig. 2 is a plan view illustrating a plurality of pixel groups which are adjacent to each other on a TFT array substrate on which data lines, scanning lines, pixel electrodes, etc., are formed. In addition, Fig. 3 is a plan view illustrating the only principal parts extracted from Fig. 2, specifically, the arrangement of the data lines, shielding layers, and pixel electrodes. Fig. 4 is a cross sectional view along the line A-A' in Fig. 2. In Fig. 4, each layer and each member are shown in different scales in order to perceive the each layer and each member in sizes recognizable in the drawing.

[0077]

In Fig. 1, in a plurality of pixels in a matrix which constitutes image display regions in the electro-optical device according to the embodiment, each of pixel electrodes 9a and each of TFTs 30 for controlling the switching of each of the pixel electrodes 9a are formed and data lines 6a to which image signals are applied is electrically connected to sources of each of TFTs 30. The image signals S1, S2, ..., Sn which are written into the data lines 6a may be line sequentially applied in this order and, otherwise, may be applied to every group of a plurality of data lines 6a adjacent to each other.

[0078]

In addition, scanning lines 3a are electrically connected to gates of the TFTs 30 and scanning signals G1, G2, ..., Gm are line sequentially applied as pulses to the scanning lines 3a in this order at a predetermined timing. The pixel electrodes 9a are electrically connected to the drains of the TFTs 30 and image signals S1, S2, ..., Sn which are applied from the data lines 6a are written at a predetermined timing by closing the switches of the TFTs 30 which are switching elements for a predetermined period.

[0079]

The predetermined levels of the image signals S1, S2, ..., Sn which are written into the liquid crystal through pixel electrodes 9a, as examples of the electro-optical materials are held for a predetermined period between the counter electrodes which are formed on the counter substrate. The liquid crystal modulates light by varying the alignment or order of molecule group in accordance with the applied voltage levels to display the gray scale. When in a normally white mode, the transmittance ratio to the incident light reduces in accordance with voltages that are applied to each pixel unit, and when in a normally black mode, the transmittance ratio to the incident light increases in accordance with voltages that are applied to each pixel unit. Totally, light having contrast according to image signals are emitted from the electro-optical

device.

[0080]

Herein, in order to prevent leakage of the held image signals, storage capacitors 70 are added parallel to the liquid crystal capacitances, which are formed between the pixel electrodes 9a and the counter electrode. The storage capacitors 70 are provided parallel to the scanning lines 3a and each of the storage capacitors contains a fixed-potential-side capacitor electrode and a capacitor electrode 300 which is fixed to a constant potential.

[0081]

Now, the real construction of the electro-optical device to which the aforementioned circuit operation is realized by the data lines 6a, the scanning lines 3a, and the TFTs 30 will be described with reference to Figs. 2 to 4.

[0082]

First, in Fig. 2, a plurality of the pixel electrodes 9a is provided in a matrix on the TFT array substrate 10 (the contours of the pixel electrodes are represented by the dot line portions 9a') and the data lines 6a and the scanning lines 3a are provided along the perpendicular and horizontal boundaries of the pixel electrodes 9a, respectively. The data lines 6a are formed in a laminated structure containing aluminum film, etc., as described

later, and the scanning lines 3a are made of conductive polysilicon film. The scanning lines 3a are provided to face the channel regions 1a' which are marked by the upward slanting line regions on the drawing within the semiconductor layers 1a. The scanning lines 3a function as gate electrodes. That is, the pixel switching TFTs 30, in which the main line portions of the scanning lines 3a as the gate electrodes face the channel regions 1a', are formed in the portions where the scanning lines 3a intersect the data lines 6a.

[0083]

Next, the electro-optical device includes TFT array substrate 10, which is made of, for example, a quartz substrate, a glass substrate, or a silicon substrate; and counter substrate 20, which is provided to face the TFT array substrate and made of, for example, a glass substrate or a quartz substrate, as shown in Fig. 4 which is a cross sectional view along the line A-A' of Fig. 2.

[0084]

As shown in Fig. 4, the aforementioned pixel electrode 9a is provided at the TFT array substrate 10, and an alignment film 16 on which a predetermined alignment process such as a rubbing process is carried out is provided above the pixel electrode. The pixel electrode 9a is made of, for example, a transparent conductive film such as an ITO film.

On the other hand, the counter electrode 21 is provided over the entire surface of the counter substrate 20, and, an alignment film 22 on which a predetermined alignment process such as a rubbing process is carried out is provided below the counter electrode. The counter electrode 21 is made of, for example, a transparent conductive film such as an ITO film similar to the aforementioned pixel electrodes 9a, and the aforementioned alignment films 16 and 22 are made of, for example, a transparent organic film such as a polyimide film. Between the TFT array substrate 10 and the counter substrate 20 which are provided to face, liquid crystal layer 50 is formed by sealing electro-optical materials such as liquid crystal within a space which is surrounded with a sealing material which is described later (see Figs. 19 and 20). The liquid crystal layer 50 is aligned by the alignment films 16 and 22 in a state where an electric field is not applied from the pixel electrode 9a. The liquid crystal layer 50 is made of, for example, electro-optical materials in which one or various kinds of nematic liquid crystal are mixed. The sealing material is an adhesive agent formed of, for example, photo-curing resin or thermosetting resin for connecting the TFT substrate 10 and the counter substrate 20 at their peripherals. Spacers such as glass fiber or glass beads for separating both substrates from each other by a predetermined distance are mixed with

the adhesive agent.

[0085]

On the other hand, on the TFT array substrate 10, besides the aforementioned pixel electrodes 9a and the alignment film 16, various constituents including above pixel electrodes and the alignment film are provided in a laminated structure. As shown in Fig. 4, the laminated structure includes, in the following order from TFT array substrate 10, a first layer having lower light-shielding film 11a, a second layer having the TFT 30 and the scanning line 3a, a third layer having the storage capacitor 70 and data line 6a, a fourth layer having shielding layer 400, and a fifth layer (the uppermost layer) having the aforementioned pixel electrode 9a and the alignment film 16. In addition, a base insulating film 12 is provided between the first layer and the second layer, a first interlayer insulating film 41 is provided between the second layer and the third layer, a second interlayer insulating film 42 is provided between the third layer and the fourth layer, and a third interlayer insulating film 43 is provided between the fourth layer and the fifth layer in order to prevent short circuit among the aforementioned elements. In addition, contact holes for electrically connecting heavily doped source regions 1d within the semiconductor layer 1a of the TFTs 30 with the data lines 6a are also provided in the

various insulating films 12, 41, 42, and 43. Now, these elements will be described in the aforementioned order from the bottom.

[0086]

First, the lower light-shielding films 11a which are made of, for example, a metal monomer, an alloy, a metal silicide, a poly silicide, or a laminated structure thereof which are including at least one of high melting point metals such as Ti (titan), Cr (chromium), W (tungsten), Ta (tantalum), and Mo (molybdenum), etc. is provided on the first layer. The lower light-shielding film 11a is patterned in lattice in plan view, thereby defining opening regions for each pixel (see, Fig. 2). In addition, in order to prevent the potential variation from having bad effect on the TFTs 30, it is preferable that the lower light-shielding film 11a extend from the image display regions to peripherals thereof to be connected to a constant potential source.

[0087]

Next, the TFTs 30 and the scanning lines 3a are provided on the second layer. The TFTs 30 have LDD (lightly doped drain) structures as shown in Fig. 4 and comprise the scanning lines 3a which function as a gate electrode as described above; the channel regions 1a' of the semiconductor layer 1a where channels are formed by electric

fields from the scanning lines 3a made of a polysilicon film; insulating films 2 having gate insulating films which insulate the scanning lines 3a from the semiconductor layer 1a; and lightly doped source regions 1b, lightly doped drain regions 1c, heavily doped source regions 1d, and heavily doped drain regions 1e in the semiconductor layer 1a.

[0088]

Furthermore, although it is preferable that the TFTs 30 have the LDD structure as shown in Fig. 4, the TFTs may have an offset structure where impurity implantation is not performed to the lightly doped source regions 1b and the lightly doped drain regions 1c. Otherwise the TFTs may be a self-aligned type TFT in which impurities are implanted heavily by using the gate electrodes which are formed to be a part of the scanning lines 3a as masks and then the heavily doped source regions and the heavily doped drain regions are formed in a self-aligned manner. In addition, in the present embodiment, although the single gate structure in which the only one gate electrode of the pixel switching TFT 30 is provided between the heavily doped source regions 1d and the heavily doped drain regions 1e is used, two or more gate electrodes may be provided between them. Likewise, when the TFT is constructed in dual gates, triple gates, or more, the leakage current at the connection portions of the channel and source regions and drain regions

can be prevented so that it is possible to reduce current in the OFF state. In addition, the semiconductor layer 1a which constructs the TFTs 30 may be a non-single crystalline layer or a single crystalline layer. Known methods such as an attaching method may be used for the formation of the single crystalline layer. In particular, by forming the semiconductor layer 1a with the single crystalline layer, it is possible to obtain high performance of peripheral circuits.

[0089]

The base insulating film 12 which is made of, for example, a silicon oxide film is provided above the aforementioned lower light-shielding films 11a and below the TFTs 30. Other than the function of insulating the interlayers from the lower light-shielding films 11a to the TFTs 30, the base insulating film 12 has a function of preventing the properties of the pixel switching TFTs 30 from varying due to the roughness which is generated at the time of polishing the surface of the TFT array substrate 10 or contaminants which remain after the cleaning by forming on the entire surface of the TFT array substrate 10.

[0090]

Furthermore, in the embodiment, particularly, in the base insulating film 12, grooves 12cv (contact holes) having the same width as the channel length are engraved in both

sides of the semiconductor layer 1a in plan view. The scanning lines 3a which are laminated corresponding to the grooves 12cv have concave portions formed at lower sides thereof (these are not shown to avoid complexity in Fig. 2). In addition, by forming the scanning lines 3a so as to bury the entire grooves 12cv, horizontal protrusions 3b, which are formed in one body with the scanning line 3a, are provided to extend to the scanning line 3a. According to such a structure, the semiconductor layer 1a of the TFT 30 is covered from the sides in plan view as well-shown in Fig. 2 so that it is possible to suppress the incident of the light from at least this part.

[0091]

Following the aforementioned second layer, on the third layer, a storage capacitor 70 and the data line 6a are provided. The storage capacitor 70 is formed by facing a first relay layer 71 as a pixel-potential-side capacitor electrode, which is electrically connected the heavily doped drain region 1e of the TFT 30 with the pixel electrode 9a, to capacitor electrode 300 as a fixed-potential-side capacitor electrode through the dielectric film 75. It is possible to greatly improve the potential holding property of the pixel electrodes 9a by the storage capacitor 70. In addition, the storage capacitors 70 according to the embodiment are formed not to reach light transmitting region

corresponding almost to region for forming the pixel electrode 9a as shown in the plan view of Fig. 2 (in other words, the storage capacitor is formed so as to be accommodated into the light-shielding regions). By doing so, the pixel aperture ratio of the entire electro-optical device is maintained to be relatively large so that it is possible to display brighter images.

[0092]

More specifically, the first relay layer 71 is made of, for example, a conductive polysilicon film and functions as a pixel-potential-side capacitor electrode. However, the first relay layer 71 may be constructed in a single layer film or a multi-layered film containing metals or alloys thereof. In addition to the function as the pixel-potential-side capacitor electrode, the first relay layer 71 has a function for relay-connecting the pixel electrode 9a with the heavily doped drain region 1e of the TFT 30 through contact holes 83, 85, and 89. The first relay layer 71 is formed to have almost the same shape as the plane shape of the capacitor electrode 300 described later as shown in Fig. 2.

[0093]

The capacitor electrode 300 functions as a fixed-potential-side capacitor electrode of the storage capacitor 70. In the first embodiment, in order to keep the capacitor

electrode 300 to be a fixed potential, the capacitor electrode is electrically connected to the shielding layer 400 which is a fixed potential.

[0094]

Moreover, in particular, in this embodiment, the data lines 6a are made of the same films as the capacitor electrodes 300. Herein, 'the same films' stand for the same layers or layers that are formed at the same time in manufacturing process steps. However, the capacitor electrodes 300 and the data lines 6a are not continuously formed in a plan view but divided therebetween based on the patterning.

[0095]

Specifically, as shown in Fig. 2, the capacitor electrodes 300 are formed to overlap the regions forming the scanning lines 3a, that is, to be divided along the X direction in the drawing, and the data lines 6a are formed to overlap the semiconductor layer 1a in the longitudinal direction thereof, that is, to extend in the Y direction in the drawing. More specifically, the capacitor electrodes 300 has main line portions which extend along the scanning lines 3a, and protrusions (portions shown as approximate a trapezoid shape in the drawing) protruded upwardly in the drawing along the semiconductor layer 1a in the regions which are adjacent to the semiconductor layer 1a in Fig. 2,

and necking portions having necking shapes corresponding to the below-described contact holes 85. The protrusions contribute to the increase of the regions forming storage capacitors 70.

[0096]

On the other hand, the data lines 6a have main line portions which extend linearly along the Y direction in Fig. 2. In addition, the upper end of the semiconductor layer 1a in Fig. 2 have a shape in which the regions are bent to the right side at 90 degrees in order to electrically connect the semiconductor layer 1a with the storage capacitors 70 while bypassing the data lines 6a (see Fig. 4).

[0097]

In the embodiment, such a patterning is performed to obtain the aforementioned shapes, and thus, the capacitor electrodes 300 and the data lines 6a are simultaneously formed.

[0098]

In addition, the capacitor electrode 300 and the data line 6a are formed of films having a two-layered structure, as shown in Fig. 4, in which the lower layer is made of conductive polysilicon and the upper layer is made of aluminum. Although the data line 6a are electrically connected to the semiconductor layer 1a of the TFT 30 through the contact hole 81 which passes through opening

portion of the below-described dielectric film 75, the data line 6a has the aforementioned two-layered structure and the aforementioned first relay layer 71 is constructed with a conductive polysilicon film so that the electrical connection between the data lines 6a and the semiconductor layer 1a can be directly implemented by the conductive polysilicon film (that is, a polysilicon film of first relay layer, a polysilicon film below the data line 6a, and an aluminum film above the data line are formed in this order from the bottom). Therefore, it is possible to maintain a good electrical connection between them. According to the embodiment, the data lines 6a and the capacitive lines 300 has a two-layer structure of the conductive poly silicon layer and the aluminum layer, however, may have a three-layer structure of the conductive poly silicon layer, the aluminum layer, and a titan nitride layer in the order from the lowermost layer. According to this structure, the titan nitride layer functions as a barrier metal for preventing the portion not to be etched from being etched when the contact hole 87 is opened.

[0099]

Since the capacitor electrode 300 and the data line 6a contain aluminum having relatively excellent light reflective property and also polysilicon having relatively excellent light absorbing property, the capacitor electrode

and the data line can function as light-shielding layers. In other words, the capacitor electrode and the data line are able to block the progress of the incident light (see Fig. 4) for the semiconductor layer 1a of the TFT 30 at the upper side thereof.

[0100]

The dielectric film 75 is constructed with a silicon oxide film such as a HTO (high temperature oxide) film, and a LTO (low temperature oxide) film, etc., or a silicon nitride film having relatively thin thickness, for example, about 5 to 200 nm, as shown in Fig. 4. In view of increasing the storage capacitor 70, the thinner dielectric films 75 are preferable as long as sufficient reliabilities of the films are obtained. According to the embodiment, in particular, the dielectric film 75 has a two-layer structure, in which a lower layer is formed of a silicon oxide film 75a and an upper layer is formed of a silicon nitride film 75b as shown in Fig. 4. The upper silicon nitride film 75b is patterned so as to be included in a light-shielding region (a non-aperture region). Therefore, it is possible to increase the capacitance of the storage capacitor 70 due to the presence of the silicon nitride film 75b with a relatively large dielectric constant. Also, the capacity of the storage capacitor 70 to withstand voltage does not deteriorate in spite of the presence of the silicon

oxide film 75a. As mentioned above, it is possible to obtain two reciprocal effects by the dielectric film 75 having the two-layer structure. Also, it is possible to prevent the transmittance rate from deteriorating because the colored silicon nitride 75b is patterned so as not to be formed in a region that light transmits. Also, it is possible to prevent in advance the TFT 30 from being submerged due to the presence of the silicon nitride film 75b. Therefore, according to the embodiment, it is possible to prevent the threshold voltage of the TFT 30 from increasing and to operate the device for a relatively long time. According to the embodiment, the dielectric film 75 has the two-layer structure. However, the dielectric film may have a three or more layer laminated structure such as the silicon oxide film, the silicon nitride film, and the silicon oxide film.

[0101]

A first interlayer insulating film 41 is formed above the TFT 30 or the scanning line 3a and below the storage capacitor 70 or the data line 6a. The first interlayer insulating film 41 is made of, for example, a silicate glass film such as NSG (non-doped silicate glass), PSG (phosphorus silicate glass), BSG (boron silicate glass), and BPSG (boron phosphorus silicate glass); a silicon nitride film; a silicon oxide film; or preferably NSG. In addition, the

contact hole 81 which electrically connects the heavily doped source region 1d of the TFT 30 with the data line 6a is opened in the first interlayer insulating film 41. Furthermore, the contact hole 83 which electrically connects the heavily doped drain region 1e of the TFT 30 with the first relay layer 71 constituting the storage capacitor 70 is opened in the first interlayer insulating film 41.

[0102]

In addition, in order not to form the aforementioned dielectric film 75 on the portion for forming the contact hole 81 from the two contact holes, opening portions are formed in the dielectric film 75. The reason for such a formation is that it is necessary to facilitate the electric conduction between the highly doped source regions 1b and the data lines 6a through the first relay layers 71 on the contact hole 81. Namely, when the opening portion is provided in the dielectric film 75, in case of performing a hydrogenation process on the semiconductor layer 1a of the TFT 30, it is possible to obtain the effect wherein hydrogen used in the process can easily reach the semiconductor layer 1a through the opening portion.

[0103]

Furthermore, in the embodiment, the first interlayer insulating film 41 may undergo a sintering process at 1000°C, and thus, the ions which are implanted into the

polysilicon film constituting the semiconductor layer 1a or the scanning line 3a may be activated.

[0104]

Following the aforementioned third layer, shielding layers 400 are provided on the fourth layer. The shielding layers 400 are formed in lattice to extend in the X and Y directions of Fig. 2 in plan view as shown in Figs. 2 and 3. The portions of the shielding layers 400 which extend in the Y direction of Fig. 2 are formed to be wider than the data lines 6a, thereby covering the data lines 6a. In addition, the portions which extend in the X direction of Fig. 2 have notched portions in the vicinity of approximately the center position of one side of each pixel electrode 9a in order to ensure regions for forming the below-described third relay electrode 402. In addition, at the corner portions of the intersections of the shielding layers 400 which extend in the X and Y directions of Fig. 2, respectively, portions which have approximately a triangle shape are provided to correspond to the protrusions of the capacitor electrodes 300 which have approximately a trapezoid shape. The almost triangular portion is also included in the shielding layer 400.

[0105]

The shielding layers 400 are provided to extend from the image display regions 10a on which the pixel electrodes

9a are provided to their peripherals, and electrically connected to the constant potential source to be at a fixed potential. In addition, 'the constant potential source' referred herein may be the constant potential source such as a positive power source or a negative power source supplied to a data line driving circuit 101, or a constant potential source supplied to the counter electrode 21 of the counter substrate 20.

[0106]

Like this, since the entire data lines 6a are covered (see Fig. 3) and the shielding layers 400 are at the fixed potential, it is possible to remove the influence of the capacitance coupling which occurs between the data lines 6a and the pixel electrodes 9a. Namely, by the electrical conduction to the data lines 6a, it is possible to prevent the variation of the potential of the pixel electrodes 9a in advance, and thus, it is possible to reduce the probability of occurrence of the display non-uniformity involved in the data lines 6a on the image. In the embodiment, since the shielding layers 400 are formed in lattice, it is possible to suppress the unnecessary capacitance couplings at the portions to which the scanning lines 3a extend. In addition, by the aforementioned portions of the triangle shape of the shielding layers 400, it is possible to prevent the influence of the capacitance coupling which occurs

between the capacitor electrode 300 and the pixel electrodes 9a, and by doing so, it is possible to obtain the substantially same effects described above.

[0107]

In addition, on the fourth layer, second relay layer 402 which is one example of 'relay layers' referred in the invention are formed of the same film as the shielding layers 400. The second relay layer 402 has a function of relaying electrical connection between the first relay layers 71 which constitute the storage capacitors 70 and the pixel electrodes 9a through the below-described contact holes 89. In addition, the shielding layers 400 and the second relay layer 402 are not formed continuous in a plan view but divided based on the patterning which is similar to the aforementioned the capacitor electrodes 300 and the data lines 6a.

[0108]

On the other hand, the shielding layers 400 and the second relay layer 402 have a two-layered structure in which the lower layer is made of aluminum and the upper layer is made of titanium nitride. Therefore, the titan nitride is expected to function as the barrier metal for preventing the portion not to be etched from being etched when the contact hole 89 is opened. Also, in the second relay layer 402, the lower layer made of aluminum is connected to the first relay

layer 71 that constitutes the storage capacitor 70. The upper layer made of the titan nitride is connected to the pixel electrode 9a made of ITO. In this case, in particular, the latter connection is excellent. When the aluminum is directly connected to the ITO, a desired electrical connection is not realized because electrical erosion occurs between the aluminum and the ITO, thereby causing the disconnection of the aluminum or insulation due to the formation of alumina. As mentioned above, according to the embodiment, it is possible to realize excellent electric connection between the second relay layer 402 and the pixel electrode 9a and thereby to maintain excellent properties of applying voltage to the pixel electrode 9a or maintaining potentials in the pixel electrode 9a.

[0109]

In addition, since the shielding layers 400 and the second relay layer 402 contain aluminum having relatively good light reflective property and also titanium nitride having relatively good light absorbing property, the shielding layers and the second relay layer can function as light-shielding layers. In other words, the shielding layers and the second relay layer are able to block the travel of the incident light (see Fig. 2) to the semiconductor layer 1a of the TFT 30 at the upper side thereof. These are similar to the capacitor electrodes 300

and the data lines 6a as described above. In the embodiment, the shielding layers 400, the second relay layer 402, the capacitor electrodes 300, and the data lines 6a constitute a part of the laminated structure which is formed on the TFT array substrate 10, thereby functioning as the upper light-shielding film which block the light incident from the upper side to the TFTs 30 (or, functioning as 'an embedded light-shielding film' in consideration of constituting 'a part of the laminated structure'). In addition, according to the concept of the 'upper light-shielding film' or the 'embedded light-shielding film,' the scanning lines 3a or the first relay layers 71 can be also included. Essentially, all the structures made of opaque materials constructed on the TFT array substrate 10 can be referred to as the 'upper light-shielding film' or the 'embedded light-shielding film' under the assumption that the upper light-shielding film or the embedded light-shielding film is to be understood in its wide meaning.

[0110]

A second interlayer insulating film 42 is formed above the data line 6a and below the shielding layer 400 and is made of a silicate glass film such as NSG, PSG, BSG, and BPSG, a silicon nitride film, a silicon oxide film, or preferably NSG. The contact hole 87 which electrically connects the shielding layer 400 and the capacitor electrode

300 and the contact hole 85 which electrically connects the second relay layer 402 with the first relay layer 71 are opened in the second interlayer insulating film 42.

[0111]

Furthermore, since the second interlayer insulating film 42 does not undergo the sintering process which is described above with respect to the first interlayer insulating film 41, it is preferable to lessen the stress which occurs in the vicinity of the boundary surface of the capacitor electrode 300.

[0112]

Finally, on the fifth layer, the pixel electrode 9a is formed in a matrix as described above and the alignment film 16 is formed on the pixel electrode 9a. In addition, a third interlayer insulating film 43 is formed below the pixel electrode 9a and is made of a silicate glass film such as NSG, PSG, BSG, and BPSG, a silicon nitride film, a silicon oxide film, or preferably BPSG. According to the embodiment, in particular, the contact hole 89 for electrically connecting the pixel electrode 9a to the second relay layer 402 is formed in the third interlayer insulating film 43. Also, the filler 409a is formed in the contact hole 89. The surface of the third interlayer insulating film 43 is planarized. The above will be described in relation to the following items.

[0113]

(Structure of Third Interlayer Insulating Film)

In the following, the structure of the above-mentioned third interlayer insulating film 43, to be specific, the structure of the contact hole formed in the third interlayer insulating film 43 will be described with reference to Figs. 4 to 9. Here, Figs. 5 to 9 have the same purpose as Fig. 4. However, differences between those figures will be mentioned hereinafter. Further, in the following, the various characteristics of the third interlayer insulating film 43 will now be described according to first to third embodiment. According to the first to third embodiment, only differences among the respective embodiments such as the structure of the filler will be mainly described. Description of the remaining structures will be simplified or omitted (The remaining structures are basically the same as the above-mentioned).

[0114]

(First Embodiment: Case where the Filler Is Formed of the Same Film as the Pixel Electrode)

According to the first embodiment, the filler 409a is included in the entire inside region of the contact holes 89 as shown in Fig. 4. To be specific, the filler 409a is formed of the same film as the pixel electrode 9a, and then the filler is made of a transparent conductive material such

as the ITO. As a result, it is possible to perform processes of forming or film forming the pixel electrode 9a and forming the filler 409a inside the contact hole 89 at the same time and thereby to reduce the manufacturing cost of the corresponding pixel electrode and the filler. According to the first embodiment, the surface of the third interlayer insulating film 43 is planarized by CMP (chemical mechanical polishing) process.

[0115]

Since the third interlayer insulating film 43 and the contact hole 89 have such structures, according to the first embodiment, a concavo-convex portion, in particular, a concave portion caused by the contact hole 89 is not formed in the surface of the alignment film 16. This is because it is possible to prevent the concave portion from being formed in the alignment film 16 due to the presence of the hollow like in the conventional art since the filler 409a is included in the entire inside region of the contact holes 89. Further, according to the first embodiment, because the third interlayer insulating film 43 is planarized, the concavo-convex portion caused by various wiring lines or elements under the third interlayer insulating film become uniform. Therefore, the flatness of the alignment film 16 is excellent. In particular, according to the electro-optical device of the embodiment, as mentioned above, the

storage capacitor 70 or the shielding layer 400 is formed in the laminated structure on the TFT array substrate 10. As a result, the step differences caused by different heights of the respective components can be more easily formed because the structure becomes more complicated. Therefore, a larger effect is obtained by planarizing the third interlayer insulating film.

[0116]

As mentioned above, according to the first embodiment, the alignment state of the liquid crystal molecules that form the liquid crystal layer 50 is not disturbed. Therefore, light leakage caused by the disturbance does not occur. As a result, it is possible to display an image with excellent quality.

[0117]

Furthermore, as a principle, it is possible to prevent light from being transmitted through the hollow of the contact hole 89 like in the conventional art due to the presence of the filler 409a according to the first embodiment (this is because 'the hollow' does not exist due to the presence of the filler 409a). Therefore, the light leakage is not caused easily. Also, according to the first embodiment, the filler 409a is made of the transparent conductive material such as the ITO. However, according to the first embodiment, it is possible to obtain the

corresponding light-shielding effect because the transparency of the transparent conductive material generally deteriorates with the growth of the thickness.

[0118]

As mentioned above, according to the first embodiment, it is possible to prevent the quality of an image from deteriorating due to the deterioration of contrast caused by the light leakage and to display an image with excellent quality.

[0119]

Furthermore, according to the first embodiment, because the contact hole 89 is filled with the filler 409a as shown in Fig. 4, it is possible to secure a larger contact area between the filler 409a and the second relay layer 402. Therefore, it is possible to deteriorate the value of resistance between the filler and the second relay layer. As a result, according to the first embodiment, it is possible to realize an excellent and firm electric connection between the pixel electrode 9a and the TFT 30.

[0120]

Further, in the above embodiment, only the contact hole 89 is filled with the filler 409a. However, the invention is not limited thereto. For example, as shown in Fig. 5, a filler 490 may be filled in the entire inside region of a contact hole 85 (corresponding to an example of 'another

contact holes' according to the invention) that may affect the shape of the surface of the alignment film 16. In this case, because the shielding layer 400 has a two-layer structure, in which a lower layer is formed of an aluminum film and an upper layer is formed of a titan nitride film, the filler 490 is preferably formed of the titan nitride film as shown in Fig. 5. The filler may be made of other materials (Please refer to the following second and third embodiment.). Furthermore, a contact hole 87 for electrically connecting the shielding layer 400 to the storage capacitor 70 may have the same structure (in Fig. 4, it seems that the contact hole is already filled). Therefore, it is possible to reduce a probability of forming the concave portion in the alignment film 16.

[0121]

Further, an example of planarizing the third interlayer insulating film 43 by the CMP process is mentioned but the invention is not limited thereto. For example, instead of or in addition to the CMP process, an etchback method may be used. Furthermore, the surface of the third interlayer insulating film 43 may be so-called 'passively' planarized by grooving at least one among the TFT array substrate 10, the base insulating film 12, the first interlayer insulating film 41, and the second interlayer insulating film 42 and by burying the wiring lines such as the data lines 6a or the

TFT 30 in the grooves instead of or in addition to 'actively' planarizing the surface of the third interlayer insulating film.

[0122]

Furthermore, 'the contact hole' according to the invention is limited to 'electrically connecting' the TFT 30 to the pixel electrode 9a. However, 'the connection' mentioned here includes cases according to the embodiment. That is, the contact hole 89 is not directly connected to the TFT 30 but is electrically 'connected' to the TFT 30 through the second relay layer 402 and the contact hole 85 and the first relay layer 71 and the contact hole 83. However, such 'a connection' is also included in 'the connection' according to the invention.

[0123]

(Second Embodiment: Case where the Filler Is Made of Light-Shielding Material)

A second embodiment will now be described hereinafter. According to the second embodiment, as shown in Fig. 6, a filler 401 is not formed of the same film as the pixel electrode 9a but is formed as a different layer. To be specific, the filler 401 according to the second embodiment is a light-shielding material such as a metal monomer including at least one among Ti (Titanium), Cr (chrome), W (tungsten), Ta (tantalum), and Mo (molybdenum); an alloy;

metal silicide; and poly silicide and is made of conductive material.

[0124]

The second embodiment has almost the same effect as the first embodiment. That is, the concave portion is not formed in the alignment film 16 because the hollow does not exist inside of the contact hole 89. Also, it is possible to realize an excellent electric contact between the second relay layer 402 and the contact hole 89.

[0125]

According to the second embodiment, in particular, it is possible to prevent the light leakage in the contact hole 89 because the filler 401 is made of the light-shielding material. That is, according to the first embodiment, the expected operation of preventing the light leakage is limited because the filler 409a is made of the transparent conductive material. However, according to the second embodiment, it is possible to prevent the light leakage because the above-mentioned light-shielding material such as Ti shields light.

[0126]

Therefore, according to the second embodiment, it is possible to prevent the quality of an image from deteriorating due to the above-mentioned operation. Also, according to the excellent light-shielding effect, it is

possible to effectively prevent light from being incident on the semiconductor layer 1a of the TFT 30. Therefore, it is possible to more effectively prevent flicker from being generated in the image due to the generation of light leakage current in the semiconductor layer 1a. Also, such an effect is obtained according to the first embodiment.

[0127]

(Third Embodiment: Case where Two-Layer Structure Is Formed inside the Contact Hole)

Hereinafter, a third embodiment will now be described. According to the third embodiment, as shown in Fig. 7, a filler 416a made of a transparent polyimide material that forms the alignment film 16 is included in the entire inside region of the contact holes 89. Also, the inner surface of the contact hole 89 is coated with a coating member 420 made of various light-shielding materials that form the filler 401 according to the second embodiment. Therefore, the coating member 420 is made of the light-shielding and conductive material.

[0128]

The third embodiment has the almost same effect as the first or second embodiment.

[0129]

According to the third embodiment, in addition to the effects of the above embodiments, the following effects are

obtained. That is, it is possible to obtain light-shielding and conductive functions by the coating member 420. Also, it is possible to form the filler 416a at the same time that the alignment film 16 is formed and thereby to reduce the manufacturing cost of the filler and the alignment film.

[0130]

Further, according to the invention, in general, the coating member 420 and the filler 416a may be made of any material. However, the coating member 420 is necessarily made of the conductive material because it is not possible to omit the original function of the contact hole for electrically connecting the pixel electrode 9a to the second relay layer 402.

[0131]

Also, the coating member is not necessarily a single layer. For example, as shown in Fig. 8, the ITO as the coating member of a first layer, which is extending from the pixel electrode 9a, corresponds to the coating member of a second layer as shown in Fig. 7 (Both of the coating members are denoted by reference numeral '420' in the drawing). The contact hole 89 in which the filler 416a is formed in the inner entire region is within the range of the invention.

[0132]

Furthermore, according to a modification of Fig. 8, for

example, as shown in Fig. 9, the coating member 420'' may be formed in the entire region on the third interlayer insulating film 43 where the pixel electrode 9a is formed. In this case, the coating member 420'' is preferably made of a transparent material. In case where the reflective type electro-optical device according to the embodiment is used (that is, in Fig. 12, where 'the incident light' and light incident on the liquid crystal layer 50 along a predetermined direction are reflected by the pixel electrode 9a and light emitted in the direction reverse to the above direction forms an image), the coating member 420'' and the pixel electrode 9a are not necessarily made of the transparent material.

[0133]

(Modifications of Electro-Optical Device)

A modification of the electro-optical device according to the invention will now be described with reference to Figs. 10 and 11. Figs. 10 and 11 are for the same purposes as Figs. 2 and 4 and are respectively a plan view and a cross-sectional view that illustrate the characteristics of the modification. Further, the structure of the pixel portion of the electro-optical device according to the modification is almost the same as the structure of the pixel portions of the electro-optical devices according to the above various embodiments. Therefore, only the specific

portions of the modification will now be described.

Description of the remaining portions will be simplified or omitted.

[0134]

In Figs. 10 and 11, there are the great difference as compared to Figs. 2 and 4 in that the capacitor electrode 300 which is the upper electrode constituting the storage capacitor 70 and the data line 6a is not made of the same film, an interlayer insulating film is added thereto (one more new 'fourth interlayer insulating film 44' is provided), and a relay electrode 719 is formed of the same film as the gate electrode 3aa. By doing so, in the following order starting from the TFT array substrate 10, a first layer which contains the lower light-shielding film 11a functioning also as scanning line, a second layer which contains the TFT 30 having the gate electrode 3aa, a third layer which contains the storage capacitor 70, a fourth layer which contains the data line 6a, a fifth layer on which a shielding layer 404 is formed, a sixth layer (the uppermost layer) which contains the aforementioned pixel electrode 9a and the alignment film 16, and the like are provided. Furthermore, the base insulating film 12 is formed between the first layer and the second layer, the first interlayer insulating film 41 is formed between the second layer and the third layer, the second interlayer

insulating film 42 is formed between the third layer and the fourth layer, the third interlayer insulating film 43 is formed between the fourth layer and the fifth layer, and the fourth interlayer insulating film 44 is formed between the fifth layer and the sixth layer, thereby preventing a short circuit between the aforementioned constituents.

[0135]

Furthermore, contact hole 801 is formed in the second interlayer insulating film 42 which is located between the aforementioned third and fourth layers, and at the same time, the relay layer 6a1 for the shielding layer is formed to correspond to the contract hole 801 on the fourth layer. And also, contact hole 803 is formed on the third interlayer insulating film 43 which is located between the aforementioned fourth and fifth layers. By doing so, the electrical connection is obtained between the shielding layer 404 and the capacitor electrodes 300 by the contact hole 801 or the relay layer 6a1 for the shielding layer and the contact hole 803.

[0136]

In Fig. 11, a heavily doped drain region 1e of a semiconductor layer 1a is electrically connected to a first relay layer 71. The relay electrode 719 formed of the same film as the gate electrode 3aa is electrically connected to the first relay layer 71. The relay electrode 719 is

electrically connected to the pixel electrode 9a.

[0137]

To be specific, the relay electrode 719 is electrically connected to the pixel electrode 9a through a second relay layer 6a2 and a third relay layer 406. The second relay layer 6a2 is formed of the same film as the data line 6a to bury a contact hole 882 opened to reach the first and second interlayer insulating films 41 and 42 through the relay electrode 719. The third relay layer 406 is formed of the same film as the shielding layer 404 to bury a contact hole 804 opened to reach the third interlayer insulating film 43 through the second relay layer 6a2.

[0138]

The data line 6a is formed of a three-layer film made of an aluminum layer, a titan nitride layer, and a silicon nitride layer in the order from a lower layer. The silicon nitride layer is patterned in a size slightly larger than the aluminum layer and the titan nitride layer thereunder so as to cover the layers. It is possible to easily supply image signals to the TFT 30 and the pixel electrode 9a because the data line 6a includes aluminum that is a material having relatively low resistance. It is possible to improve the moisture proof property of the TFT 30 and thereby to prolong the life of the TFT because the silicon nitride film that prevents the permeation of moisture is

formed on the data line 6a. The silicon nitride film is preferably a plasma silicon nitride film.

[0139]

In the above case, the third relay layer 406 is preferably formed of the aluminum film and the titan nitride film as mentioned above because electrical erosion may occur between the third relay layer 406 and the ITO of the pixel electrode 9a. If necessary, the shielding layer 404 and the third relay layer 406 may be formed not to have an island shape but to have a mat shape over the entire surface of the substrate. The shielding layer and the third relay layer may be made of the ITO in order to prevent deterioration of the transmittance ratio and they may result in a new shielding layer. In the above case, a hole is formed in the shielding layer in a mat shape so as to form the contact hole 89 such that the contact hole 89 is connected to the second relay layer 6a2. Furthermore, in the above case, the relay layer 6a1 for the shielding layer is not necessary. Moreover, it is possible to prevent the shielding layer formed in the mat shape from being directly connected to the second relay layer 6a2 and thereby to prevent the electrical erosion from occurring.

[0140]

On the other hand, the electrical connection between the relay electrode 719 and the first relay layer 71 is

performed through contact hole 881 which is opened in the first interlayer insulating film 41. Namely, after opening the contact hole 881, the precursor film of the first relay layer 71 is formed to bury the contact hole, so that the electrical connection between the first relay layer 71 and the relay electrode 719 can be realized.

[0141]

By doing so, the electrical connection is formed between the first relay layer 71 and the pixel electrode 9a through the relay electrode 719.

[0142]

Additionally, although in the aforementioned embodiment the scanning line 3a is formed to include the gate electrode in the same plane, in this aspect, the lower light-shielding film 11a in the aforementioned embodiment functions as the scanning line in order to obtain the regions in which the relay electrode 719 is formed. The lower light-shielding film 11a according to the embodiment is formed in stripes in a plane. The bottom of a groove (a contact hole) 12cv as wide as the channel length is formed on both sides of the semiconductor layer 1a to contact the lower light-shielding film 11a. A scanning signal is supplied from the lower light-shielding film 11a to a gate electrode 3aa. Therefore, according to the embodiment, the horizontal protrusion 3b shields the semiconductor layer 1a from light

and also supplies signals to the gate electrode 3aa.

[0143]

Furthermore, the relay electrode 719 is formed in an island shape to be located at the approximately center position of one side of each pixel electrode 9a in plan view as shown in Fig. 10. Since the relay electrode 719 and the gate electrode 3aa are formed of the same film, when the latter is made of, for example, a conductive polysilicon film, etc., the former is also is made of a conductive polysilicon film, etc.

[0144]

According to the present aspect, similar to the first embodiment, the entire inside region of the contact holes 89 is filled with the filler 409a made of the transparent conductive material such as the ITO. Further, according to the present aspect, the surface of the fourth interlayer insulating film 44 that is an insulating film positioned under the pixel electrode 9a is planarized by the CMP process.

[0145]

According to a modification having the above-mentioned shape, it is possible to obtain similar effects caused by the presence of the filler 409a inside the contact hole 89. That is, it is possible to prevent the formation of the concavo-convex portion on the surface of the alignment film

16, in particular, the concave portion caused by the contact hole 89 due to the presence of the filler 409a and to secure the excellent flatness of the alignment film 16. Also, it is possible to prevent light from being transmitted through the hollow of the contact hole 89 as a principle.

Therefore, according to the present modification, the alignment state of the liquid crystal molecules that form the liquid crystal layer 50 is not disturbed. Thus, the light leakage caused by the disturbance does not occur. It is possible to obtain the light-shielding effect due to the presence of the filler 409a and thereby to display an image with excellent quality.

[0146]

Also, according to the present modification, it is possible to obtain a similar effect caused by the electro-optical device according to the embodiment, that is, the effect of excluding the influence of capacitance coupling between the data line 6a and the pixel electrode 9a due to the presence of the shielding layer 404.

[0147]

Also, the present modification may include the characteristics of the above-mentioned second and third embodiment. That is, the contact hole 804 as well as the contact hole 89 is filled with the filler, the filler is made of the light-shielding material (the second

embodiment), and the two-layer structure is formed inside the contact hole (the third embodiment). Therefore, it is possible to obtain almost the same effects mentioned in the corresponding parts.

[0148]

In above aspect, since the relay electrode 719 is particularly formed, it is possible to obtain the following functions and effects. Namely, in Fig. 4, etc., it is necessary to facilitate the contact with 'the upper surface' in the drawing of the first relay layer 71 which are the electrodes of the further lower layer which constitutes the storage capacitor 70 similar to the contact hole 85 in the same drawing in order to facilitate the electrical connections between the TFT 30 and the pixel electrode 9a.

[0149]

However, in such a configuration, a very difficult manufacturing process for etching the precursor film is performed with respect to the processes of forming the capacitor electrode 300 and the dielectric film 75, when etching the precursor film while the first relay layers 71 just below the precursor film remains in unimpaired condition. In particular, in a case wherein the high dielectric constant materials are used as the dielectric film 75 according to the invention, the etching is in general difficult, and since the etching rate of the

capacitor electrode 300 and the etching rate of the high dielectric constant materials are unbalanced and these conditions are overlapped, the manufacturing process is further difficult. Therefore, in this case, the probability of the so-called 'penetration' in the first relay layer 71 becomes high. If so, there are some bad cases that the short circuit may occur between the capacitor electrode 300 and the first relay layer 71 constituting the storage capacitor 70.

[0150]

However, when the electrical connection between the TFT 30 and the pixel electrode 9a is realized by providing the electrical connection points at the 'lower surface' in the figure of the first relay layer 71 as this aspect, the aforementioned unbalance does not occur. The reason is that the process of remaining the first relay layers 71 while etching the precursor film of the capacitor electrode 300 and the dielectric film 75 is not necessary in this aspect as clearly shown in Fig. 11.

[0151]

According to this aspect, since the difficult etching process is not necessary as described above, it is possible to realize a good electrical connection between the first relay layer 71 and the pixel electrode 9a. The reason is that the electrical connection between both of the first

relay layer and the pixel electrode is implemented through the relay electrode 719. In addition, for the same reason, according to this aspect, the probability that short circuit occurs between the capacitor electrodes 300 and the first relay layers 71 becomes very low. Namely, it is possible to form non-defective storage capacitor 70 appropriately.

[0152]

In addition, in this aspect, since the capacitor electrode 300 and the data line 6a are formed on the separate layers, it is not necessary to facilitate the electrical insulation between them in the same plane similar to Fig. 2, etc. Therefore, in this aspect, the capacitor electrode 300 can be formed as a part of the capacitor line which extends in the direction of the lower light-shielding film 11a (that is, the direction of the 'scanning lines 3a' which corresponds to the aforementioned aspects). By doing so, in order to provide the capacitor electrodes 300 with a fixed potential, it is preferable that the capacitor lines extend outside image display regions 10a to be connected with a constant potential source. In addition, in this case, since the capacitor lines which contain the capacitor electrodes 300 can be connected to the constant potential source independently and the shielding layer 404 can be also connected to the constant potential source independently, in case of employing this construction, the contact holes 801

and 803 for electrically connecting both constituents are not always necessary.

[0153]

(Fourth Embodiment: Case where Convex Portion Is Actively Formed in the Interlayer Insulating Film under Pixel Electrode)

Hereinafter, a fourth embodiment of the invention will be described with reference to Figs. 12 to 14. Fig. 12 has the same purpose as Fig. 11 according to the electro-optical device of the above modification and is shows a convex portion for preventing the generation of a transverse electric field. Fig. 13 is the cross-sectional view taken along the line G-G' of Fig. 10 in case where the convex portion is formed. Fig. 14 is a explanatory view for explaining a mechanism for generating the transverse electric field.

[0154]

According to the fourth embodiment, the characteristics of the surface of the fourth interlayer insulating film 44 as well as the characteristics of the filler inside the contact hole are described. That is, according to the fourth embodiment, as illustrated in Figs. 12 and 13, a convex portion 430 is formed in the surface of the fourth interlayer insulating film 44 along the scanning line 3a. The convex portion 430 has the following effects.

[0155]

Namely, in the electro-optical device of the embodiment, in order to prevent the deterioration of electro-optical materials due to the application of direct voltages and cross-talk or flicker in the image display, the inversion driving manner in which the polarity of the voltage applied to each of the pixel electrode 9a is inverted in accordance with a predetermined rule are generally employed. More specifically, the so-called '1H inversion driving manner' is described as follows.

[0156]

First, as shown in Fig. 14 (a), during the period in which image signals of the n -th field or frame are displayed (herein, n is a natural number), the polarity of the liquid crystal driving voltage which is represented by + or - for every pixel electrode 9a is not inverted, and the pixel electrode 9a is driven with the same polarity for every row. After that, as shown in Fig. 14 (b), at the time that image signal of the $(n + 1)$ th field or one frame is displayed, the voltage polarity of the liquid crystal voltage for every pixel electrode 9a is inverted. During the period in which image signals of the $(n + 1)$ th field or one frame are displayed, the polarity of the liquid crystal driving voltage which is represented by + and - for every pixel electrode 9a is not inverted, and the pixel electrodes 9a

are driven with the same polarity for every row. And then, the states shown in Fig. 14 (a) and Fig. 14 (b) are repeated by the period of one field or one frame. Namely, this is the driving in accordance with the 1H inversion driving manner. As a result, it is possible to prevent the deterioration of liquid crystal due to the application of direct voltages and simultaneously to perform the image display with reduced cross-talk or flicker. According to the 1H inversion driving manner, it is advantageous that there is almost no longitudinal cross-talk in comparison to the below-described 1S inversion driving manner.

[0157]

However, as known in Fig. 14 (a) and Fig. 14 (b), in the 1H inversion driving manner, a transverse electric field is generated between the pixel electrodes 9a which are adjacent in the longitudinal direction (Y direction) in the drawings. In the drawings, the transverse electric field generating regions C1 are always in the vicinity of the gap between the pixel electrodes 9a which are adjacent in the Y direction. When the transverse electric field is applied with regard to the electro-optical material wherein the longitudinal electric field (that is, the electric field perpendicular to the surface of the substrate) generated between the pixel electrodes and the counter electrode which faces to each other is considered to be applied, the

operational failure of the electro-optical material such as a alignment failure of the liquid crystal occurs and the light leakage occurs at the portions so that there is the problem of reduction in contrast rate.

[0158]

Although the region where the transverse electric field is generated is covered and shielded by the light-shielding films, another problem in that the opening area of the pixels is reduced in accordance with the area of the regions where the transverse electric field is generated. In particular, in accordance with the miniaturization of the pixel pitch, the distance between the adjacent pixel electrodes is further reduced and the transverse electric field increases. Therefore, these problems are getting more severe as the electro-optical devices are formed with the higher accuracy.

[0159]

According to the fourth embodiment, the convex portion 430 horizontally extending in stripes is formed in the fourth interlayer insulating film 44 between pixel electrodes 9a vertically adjacent to each other as shown in Fig. 14 (that is, adjacent pixel electrodes 9a to which potentials with reverse polarities are applied). It is possible to strengthen a vertical electric field and to weaken a transverse electric field in a connection point

between the pixel electrodes 9a disposed on the convex portion 430 due to the presence of the convex portion 430. To be specific, as illustrated in Figs. 12 and 13, the distance between the connection point between the pixel electrodes 9a disposed on the convex portion 430 and the counter electrode 21 is reduced to the height of the convex portion 430. Therefore, it is possible to strengthen the vertical electric field between the pixel electrodes 9a and the counter electrode 21 in a region C1 where the transverse electric field is generated as shown in Fig. 14. In Figs. 12 and 13, because the distance between the adjacent pixel electrodes 9a is uniform, the magnitude of the transverse electric field strengthened as the distance is narrower is uniform.

[0160]

Accordingly, in the transverse electric field generating regions C1 as shown Fig. 14, it is possible to prevent the alignment failure of the liquid crystal due to the transverse electric field by further dominating the longitudinal electric field. In addition, by the presence of the convex portions 430 which are made of an insulating film, since the intensity of the transverse electric field is lessened and the portions of the liquid crystal to which the transverse electric field is applied are reduced by the portions which are substituted with the convex portions 430

where the transverse electric field exists, it is possible to reduce the influence of the transverse electric field to the liquid crystal layer 50.

[0161]

Also, for example, the convex portion 430 is formed as illustrated in Figs. 15 and 16. Fig. 15 is a perspective view of the data line and the components formed in the same layer as the data line in the electro-optical device according to the second embodiment described above. Fig. 16 is a perspective view of the shielding layer and the components formed in the same layer as the shielding layer in the same electro-optical device.

[0162]

In order to form the convex portion 430, first, as shown in Fig. 15, the data line 6a, the relay layer 6a1 used as the shielding layer, and the second relay layer 6a2 that are formed in the electro-optical device according to the above-mentioned modification may be used. That is, as shown in Fig. 10, the data line 6a includes a main line portion straightly extending in the Y direction in Fig. 10. The relay layer 6a1 used as the shielding layer and the second relay layer 6a2 are formed to overhang from the data line 6a in the X direction in Fig. 10. It is possible to naturally form the convex portion 430 in the surface of the fourth interlayer insulating film 44 as the base of the pixel

electrodes 9a using the data line 6a, the relay layer 6a1 used as the shielding layer, and the second relay layer 6a2 due to the heights of the data line, the relay layer used as the shielding layer, and the second relay layer (with reference to Fig. 15). In this case, 'the overhanging portion' according to the invention corresponds to the relay layer 6a1 used as the shielding layer and the second relay layer 6a2 described earlier.

[0163]

Second, as shown in Fig. 16, the shielding layer 404 and the third relay layer 406 that are formed in the electro-optical device according to the aforementioned modification may be used. That is, as shown in Fig. 5, the shielding layer 404 is formed latticed and the third relay layer 406 is formed of the same layer as the shielding layer 404. It is possible to naturally form the convex portion 430 in the surface of the fourth interlayer insulating film 44 as the base of the pixel electrode 9a using the shielding layer 404 and the third relay layer 406 due to the heights of the shielding layer and the third relay layer (with reference to Fig. 16). In this case, 'the overhanging portion' according to the invention corresponds to the portion extending in the X direction of the shielding layer 404, which is connected to the portion extending in the Y direction in the shielding layer 404 illustrated in Fig. 5.

[0164]

In the above-mentioned cases, it is preferable that the surface of the interlayer insulating film formed as the base of the data line 6a or the shielding layer 404 be appropriately planarized. By doing so, it is possible to strictly define the height of the convex portion 430. The aspect of forming the convex portion using the shielding layer or the data line can also be applied to the electro-optical device above illustrated in Figs. 2 to 4.

[0165]

Furthermore, it is preferable that the step difference formed by the convex portions 430 is gentle. The 'gentle' convex portions may be implemented, for example, the following processes in which the steep convex portions are formed, a planarization film is formed on the steep convex portions and the peripherals thereof, the planarization film is removed, and an etchback process in which the surfaces of the convex portions which are exposed after the removing of the planarization film is recessed is performed.

[0166]

If the 'gentle' convex portions are provided, the rubbing process on the alignment film 16 can be relatively easily and excellently performed without any non-uniformity, so that it is possible to effectively prevent the operational failures of the electro-optical materials such

as the alignment failure of the liquid crystal in advance. When the corner of the surface of the convex portions varies steeply, the discontinuous planes occur in the electro-optical materials such as liquid crystal and that case is different from the occurrence of the operational failures of the electro-optical materials such as the alignment failure of liquid crystal, etc.

[0167]

In addition, although the aforementioned case is described about the 1H inversion driving manner, the invention is not limited to such a driving manner. For example, the invention may be adapted to the 1S inversion driving manner in which, while the pixel electrodes in the same column are driven with the potential of the same polarity, the voltage polarity is inverted at the frame or field period for every column, although this manner is used as an inversion driving manner that can be relatively easily controlled and display high quality of images. In addition, the invention may be also adapted to the recently developed dot inversion driving manner in which the voltage polarity applied to each of the pixel electrodes is inverted between the pixel electrodes which are adjacent to the bi-directions of the column and row directions.

[0168]

(Fifth Embodiment: Multiplication-speed Field Inversion

Driving)

A fifth embodiment according to the invention will now be described with reference to Figs. 17 and 18. Fig. 17 is a timing chart of a scanning signal, which illustrates a conventional method of applying voltage to the pixel electrode 9a. Fig. 18 is a timing chart according to the fifth embodiment. Further, the pixel portion described with reference to Figs. 1 to 4 is 'driven' on the basis of such timing charts.

[0169]

According to the fifth embodiment, the characteristics of the method of driving the pixel electrode 9a are described. In particular, the effects of the method are obtained in case where the surface of the interlayer insulating film under the pixel electrode 9a is planarized according to the embodiment.

[0170]

The method of applying voltage to the pixel electrode 9a according to the fifth embodiment will now be simply described using the timing chart illustrated in Fig. 17. As illustrated in the drawing, the scanning lines 3a from the first row to the last row, to which the scanning signals G1, G2, ..., and Gm are sequentially applied, are selected (with reference to Fig. 1). Here, the 'selection' means that electricity can be turned on in the TFTs 30 connected to the

scanning lines 3a. In a period (a horizontal scanning period (1H)) when the scanning lines 3a of the respective rows are selected, the image signals S1, S2, ..., and Sn are sent to the TFTs 30, further, to the pixel electrodes 9a through the data lines 6a (which is not illustrated in Fig. 17). Therefore, each pixel electrode 9a has a predetermined potential. As a result, a predetermined potential difference is generated between the potential of the counter electrodes 21 (that is, predetermined charges are charged to the liquid crystal layer 50).

[0171]

Furthermore, the period when all of the scanning lines 3a from the first row to the last row are selected is referred to as a one field period or a one vertical scanning period (1F). Also, according to the driving method, driving with inversed polarities is performed between the nth field and the (n + 1)th field (hereinafter, '1V inversion driving' is mentioned with reference to 'G1' of Figs. 17 and 18).

[0172]

According to the 1V inversion driving, unlike the above-mentioned 1H inversion driving, because the adjacent pixel electrodes 9a are not driven in the electric field with different polarities, as a principle, the transverse electric field is not generated. Therefore, according to the embodiment, even when the surface of the interlayer

insulating film under the pixel electrode 9a is planarized, it is not necessary to consider the inconveniences caused by the generation of the transverse electric field such as formation of the convex portion as described above.

[0173]

However, when the above-mentioned 1V inversion driving is adopted, the following problems occur. Whenever the polarity is inverted, that is, for every one vertical scanning period, flicker is generated on the image.

[0174]

In this case, the multiplication-speed field inversion driving as shown in Fig. 18 is preferably performed. The multiplication-speed field inversion driving is a driving method of reducing one field period to a half, compared to that of the conventional art (for example, when the pixel electrodes are driven at 120 [Hz] according to the conventional art, 'the half' is preferably no more than 1/60 [s]). Therefore, according to the 1V inversion driving, the period of the polarity inversion is half of that of the conventional art. When Fig. 18 is compared with Fig. 17, the former is shorter than the latter by the one horizontal scanning period (1H). Therefore, the one vertical scanning period (1F) becomes shorter (to be specific, as illustrated in the drawing, the one vertical scanning period is reduced to '1/2').

[0175]

By doing so, the one vertical scanning period is reduced. That is, the screen caused by the plus polarity and the screen caused by the minus polarity are swiftly exchanged. Therefore, the flicker does not appear.

[0176]

As mentioned above, according to the multiplication-speed field inversion driving method, it is possible to display an image with excellent quality without the flicker.

[0177]

Also, according to the multiplication-speed field inversion driving method, it is possible to relatively improve the potential holding property of the respective pixel electrode 9a compared with cases where other driving methods are used. This is because that the length of the one field period is reduced to half means that the time when the pixel electrodes 9a must hold predetermined potential is reduced to half of that of the conventional art. According to the embodiment, the high performance storage capacitor 70 is included in each pixel. Therefore, it is possible to prevent in advance the attenuation of voltage during the field period. The effect of improving the relative potential holding property serves for the object of displaying the image with excellent quality.

[0178]

(Entire Structure of Electro-Optical Device)

The whole construction of the electro-optical device for each of the embodiments will be described with reference to Figs. 19 and 20. In addition, Fig. 19 is a plan view illustrating the TFT array substrate and each constituent formed thereon as viewed from the counter substrate 20 side, and Fig. 20 is a cross sectional view along the line H-H' in Fig. 19.

[0179]

In Figs. 19 and 20, with regard to the electro-optical device according to the embodiment, the TFT array substrate 10 and the counter substrate 20 are provided to face each other. Between the TFT array substrate 10 and the counter substrate 20, liquid crystal 50 are sealed, and the TFT array substrate 10 and the counter substrate 20 are attached to each other with sealing materials 52 which are provided at the sealing regions where are located around the image display regions 10a.

[0180]

The sealing materials 52 are made of, for example, a ultra-violet curing resin or a thermo setting resin for attaching both substrates to each other and are cured by ultra-violet irradiation or heating, etc. Furthermore, among the sealing materials 52, gap materials (spacers) such as glass fibers or glass beads may be dispersed in order to

keep the distance (the gap between the substrates) between both substrates in a predetermined value when the liquid crystal device in the embodiments is a small liquid crystal device with a function of displaying enlarged images such as a projector. In addition, the gap materials may be included in the liquid crystal layer 50 when the liquid crystal device is a large liquid crystal device, such as a liquid crystal display, LCD TV, or the like, with function of displaying images in the equal magnification power.

[0181]

In the outside regions of the sealing materials 52, a data line driving circuit 101 which drives the data lines 6a by supplying the image signals to the data lines 6a at a predetermined timing and an external circuit connection terminals 102 are provided along the one side of the TFT array substrate 10, and scanning line driving circuits 104 which drive the scanning lines 3a by supplying the scanning signals to the scanning lines 3a at a predetermined timing is provided along the two sides which are adjacent to the one side.

[0182]

In addition, as long as the delay of the scanning signals supplied to the scanning lines 3a is not considered to be a problem, the scanning line driving circuits 104 may be also provided at the only one side. Furthermore, the

data line driving circuit 101 may be provided at both sides along the one side of the image display regions 10a.

[0183]

In the other side of the TFT array substrate 10, a plurality of wiring lines 105 is provided to connect the scanning line driving circuits 104 which are provided at both sides of the image display regions 10a. Furthermore, in at least one position of the corner portions of the counter substrate 20, a conductive material 106 is provided to perform electrical conduction between the TFT array substrate 10 and the counter substrate 20.

[0184]

In Fig. 20, on the TFT array substrate 10, alignment films are provided over the pixel switching TFTs or the pixel electrodes 9a after forming the wiring lines such as scanning lines, data lines, or the like. On the other hand, on the counter substrate 20, alignment films as well as the counter electrode 21 is provided on portions of the uppermost layer. Furthermore, the liquid crystal layer 50 is formed of, for example, a liquid crystal compounded with one or various kinds of nematic liquid crystal, and take predetermined states between a pair of the alignment films.

[0185]

In addition, on the TFT array substrate 10, a sampling circuit for applying the image signals to the plurality of

the data lines 6a in a predetermined timing, a precharge circuit for supplying precharge signals of a predetermined voltage level to the plurality of the data lines 6a in advance of the image signals, and a test circuit for checking the quality, defects, etc., of the electro-optical device during the manufacturing stage or at the shipment stage as well as the data line driving circuit 101, the scanning line driving circuits 104, and the like may be provided.

[0186]

(Electronic Apparatus)

Next, with regard to an embodiment of a projection type color display device as an example of the electronic apparatus for which the electro-optical device described above in details is used as a light valve, the whole construction, and particularly the optical construction will be described. Herein, Fig. 21 is a schematic cross sectional view of the projection type color display device.

[0187]

In Fig. 21, the liquid crystal projector 1100 as an example of the projection type color display device according to the embodiment is provided with three liquid crystal modules which contain liquid crystal devices in which driving circuits are mounted on the TFT array substrate, and is constructed as a projector which is used

as RGB light valves 100R, 100G, and 100B, respectively. In the liquid crystal projector 1100, when the projection light is emitted from the a lamp unit 1102 of a white light source such as a metal halide lamp, etc., the light is divided into light components R, G, and B corresponding to the three primary colors of RGB by three pieces of mirrors 1006 and two pieces of dichroic mirrors 1108, and then guided to the light valves 100R, 100G, and 100B corresponding to each color. At this time, in particular, the B light is guided through relay lens system 1121 which includes an incident lens 1122, a relay lens 1123, and an emitting lens 1124 in order to prevent light leakage due to long light paths. In addition, the light components corresponding to the three primary colors which are modulated by the light valves 100R, 100G, and 100B, respectively, are combined again by a dichroic prism 1112, and then projected as a color image on a screen 1120 through the projection lens 1114.

[0188]

The invention is not limited to the aforementioned embodiments, and can be appropriately modified without departing from the principle or spirit of the invention drawn out from the claims and the whole specification, and any electro-optical device and electronic apparatus involved in the modifications are also included into the technical scope of the invention. The electro-optical device can be adapted

to an electrophoresis device, an EL (electroluminescent) device, and the like.

[Brief Description of the Drawings]

[Fig. 1]

Fig. 1 is a circuit diagram illustrating an equivalent circuit of various elements and wiring lines that are provided in a plurality of pixels in a matrix that constitutes image display regions in an electro-optical device according to an embodiment of the invention.

[Fig. 2]

Fig. 2 is a plan view illustrating a plurality of pixel groups which is adjacent to each other on a TFT array substrate on which data lines, scanning lines, pixel electrodes, etc. are formed in the electro-optical device according to the embodiment of the invention.

[Fig. 3]

Fig. 3 is a plan view mainly illustrating the data lines, the shielding layer, and the pixel electrodes in Fig. 2 in order to illustrate the arrangement relationship among them.

[Fig. 4]

Fig. 4 is a cross-sectional view taken along the line A-A' of Fig. 2.

[Fig. 5]

Fig. 5 is a cross-sectional view taken along the line

A-A', which illustrates a modification of Fig. 3.

[Fig. 6]

Fig. 6 is related to the second embodiment of the invention and has the same purpose as Fig. 3, however, is a cross-sectional view taken along the line A-A', which illustrates that the filler inside the contact hole is made of a different material from that of Fig. 3.

[Fig. 7]

Fig. 7 is related to the third embodiment of the invention and has the same purpose as Fig. 3, however, is a cross-sectional view taken along the line A-A', which illustrates that Fig. 7 is different from Fig. 3 in that a coating member is disposed inside of the contact hole.

[Fig. 8]

Fig. 8 is a cross-sectional view taken along the line A-A', which illustrates a modification where a two-layer coating member is disposed in Fig. 7.

[Fig. 9]

Fig. 9 is a cross-sectional view taken along the line A-A', which illustrates a modification, in which the coating member is formed to the region where the pixel electrode is formed, in Fig. 8.

[Fig. 10]

Fig. 10 has the same purpose as Fig. 2 and illustrates an aspect where the storage capacitors and the data lines

are formed in a different layer.

[Fig. 11]

Fig. 11 has the same purpose as Fig. 4 and illustrates an aspect where the storage capacitors and the data lines are formed in a different layer.

[Fig. 12]

Fig. 12 has the same purpose as Fig. 11 and illustrates an aspect where a convex portion for preventing the generation of a transverse electric field is formed.

[Fig. 13]

Fig. 13 is a cross-sectional view taken along the line G-G' of Fig. 10 in case where the convex portion for preventing the generation of the transverse electric field is formed.

[Fig. 14]

Fig. 14 is an explanatory view for explaining the generation mechanism of the transverse electric field.

[Fig. 15]

Fig. 15 is a perspective view illustrating a specific aspect for forming the convex portion illustrated in Figs. 12 and 13 (an aspect where the data lines, the relay layer used as the shielding layer, and the second relay layer are used).

[Fig. 16]

Fig. 16 is a perspective view illustrating a specific

aspect for forming the convex portion illustrated in Figs. 12 and 13 (an aspect where the shielding layer and the third relay layer are used).

[Fig. 17]

Fig. 17 is a timing chart illustrating a method of applying voltage to the pixel electrodes according to the conventional art.

[Fig. 18]

Fig. 18 is a timing chart illustrating a method of applying voltage to the pixel electrodes according to the fifth embodiment of the invention.

[Fig. 19]

Fig. 19 is a plan view illustrating a TFT array substrate in the electro-optical device according to the embodiments of the invention together with the constituents formed on the substrate as viewed from the counter substrate.

[Fig. 20]

Fig. 20 is a cross-sectional view taken along the line H-H' of Fig. 19.

[Fig. 21]

Fig. 21 is a schematic cross-sectional view illustrating a color liquid crystal projector that is an example of a projection type color display device that is an embodiment of the electronic apparatus according to the

invention.

[Reference Numerals]

- 1a: SEMICONDUCTOR LAYER
- 1a': CHANNEL REGION
- 2: INSULATING FILM
- 3a: SCANNING LINE
- 3b: HORIZONTAL PROTRUSION (INCLUDING VERTICAL PROTRUSION)
- 6a: DATA LINE
- 9a: PIXEL ELECTRODE
- 10: TFT ARRAY SUBSTRATE
- 11a: LOWER LIGHT-SHIELDING FILM
- 12: BASE INSULATING FILM
- 16: ALIGNMENT FILM
- 20: COUNTER SUBSTRATE
- 21: COUNTER ELECTRODE
- 22: ALIGNMENT FILM
- 30: TFT
- 43: THIRD INTERLAYER INSULATING FILM
- 430: CONVEX PORTION
- 50: LIQUID CRYSTAL LAYER
- 70: STORAGE CAPACITOR
- 75: DIELECTRIC FILM
- 75a: SILICON OXIDE FILM
- 75b: SILICON NITRIDE FILM

81, 82, 83, 85, 87, 89: CONTACT HOLES
300: CAPACITOR ELECTRODE
400: SHIELDING LAYER
402: SECOND RELAY LAYER
401, 409a, 416a: FILLERS
420: COATING MEMBER

[Name of Document] ABSTRACT

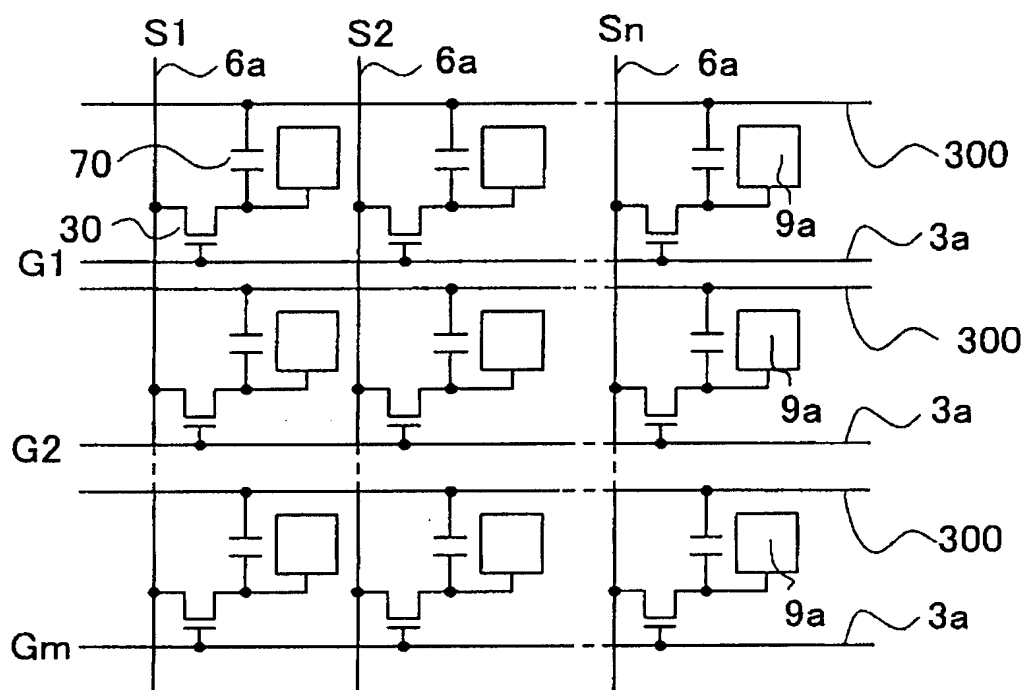
[Abstract]

[Object] To allow an electro-optical device to display an image with high quality by maintaining the flatness of a laminated structure on a substrate and also preventing light leakage caused by contact holes formed in the laminated structure.

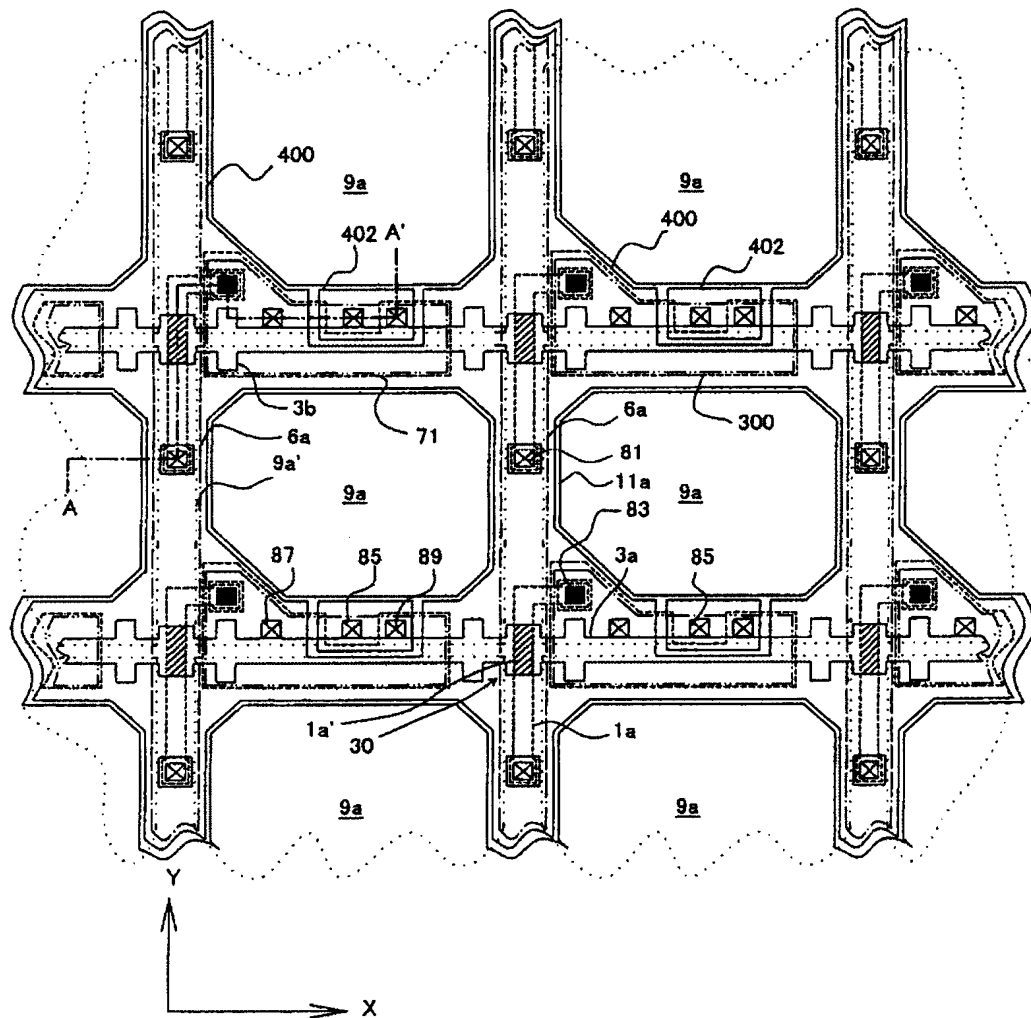
[Solving Means] On a substrate, data lines (6a), scanning lines (3a), pixel electrodes (9a), and TFTs (30) are formed so as to constitute a part of a laminated structure. Moreover, storage capacitors (70) electrically connected to the TFTs and the pixel electrodes, shielding layers (400) disposed between the data lines and the pixel electrodes, and interlayer insulating films (43) disposed as the base of the pixel electrodes are included on the substrate so as to constitute a part of the laminated structure. Among above, contact holes (89) electrically connected to the TFTs and the pixel electrodes may be included in the interlayer insulating films and the entire inside region thereof is filled with fillers (409a) made of conductive materials. The surface of the interlayer insulating films is planarized.

[Selected Figure] Fig. 4

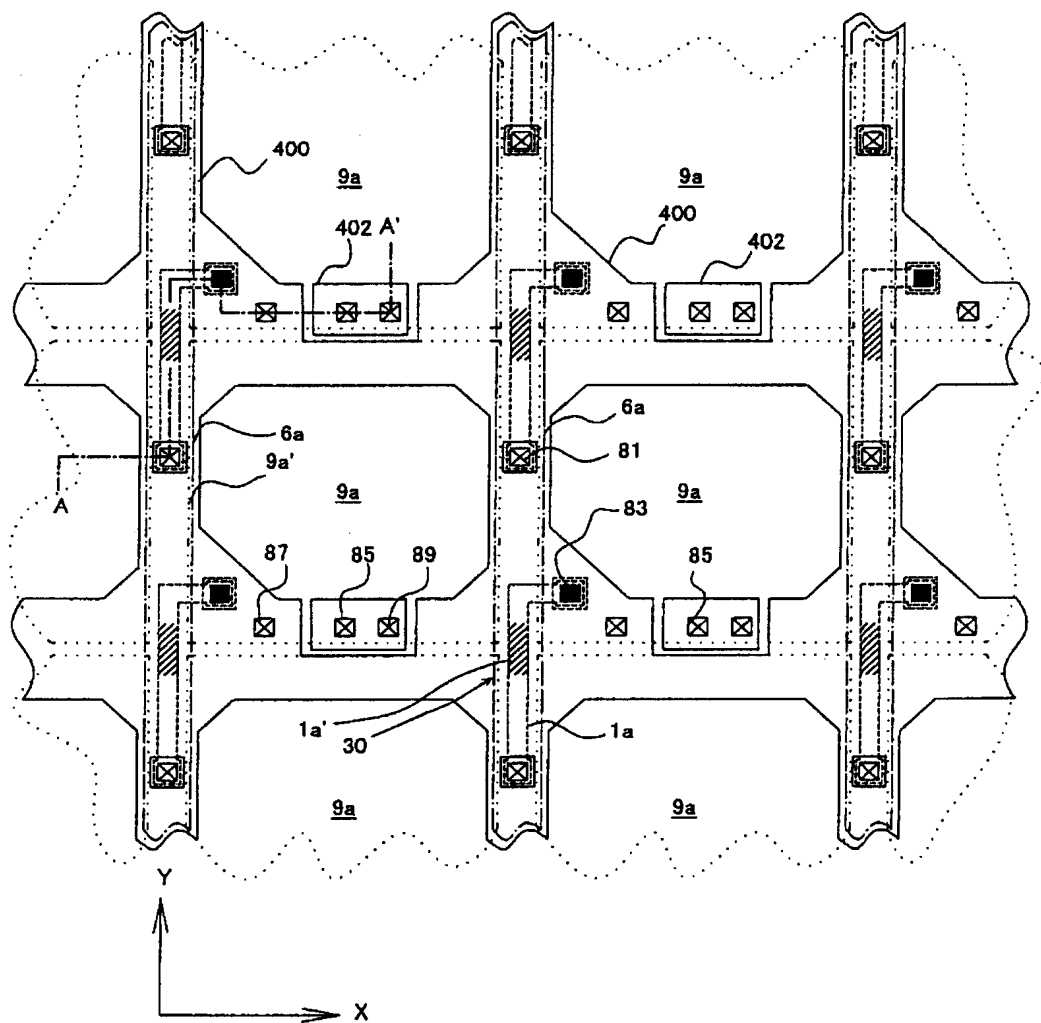
[FIG. 1]



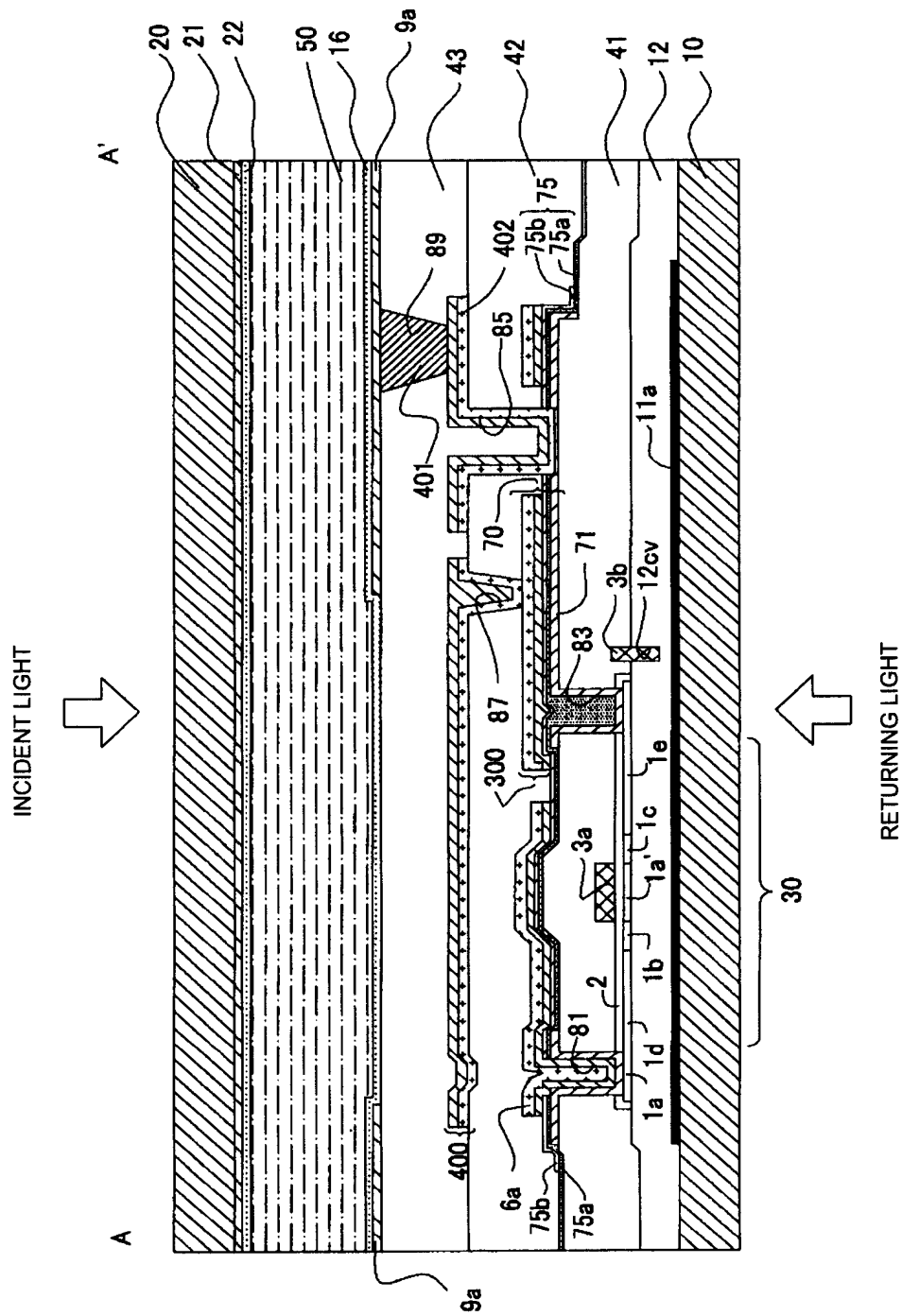
[FIG. 2]



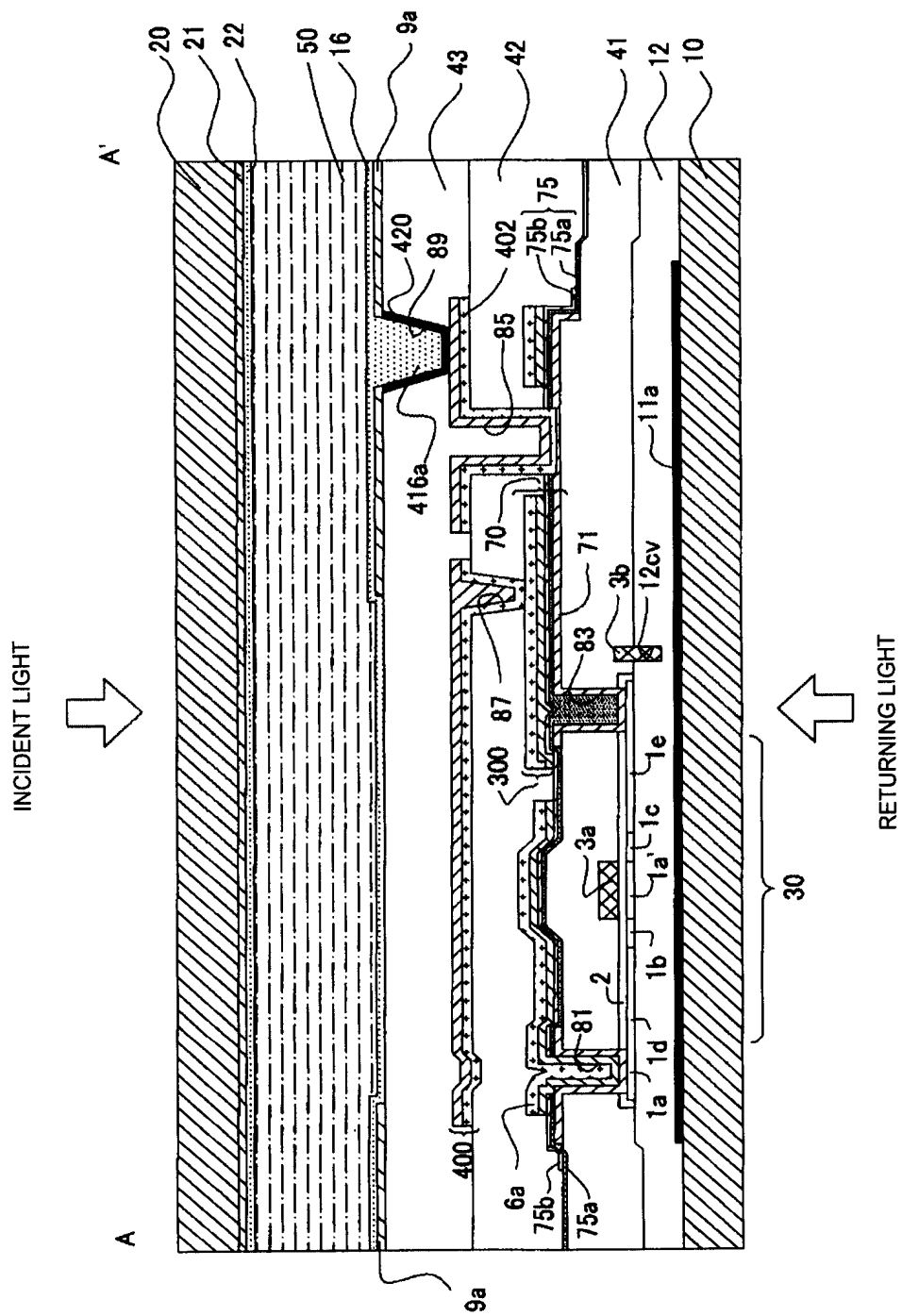
[FIG. 3]



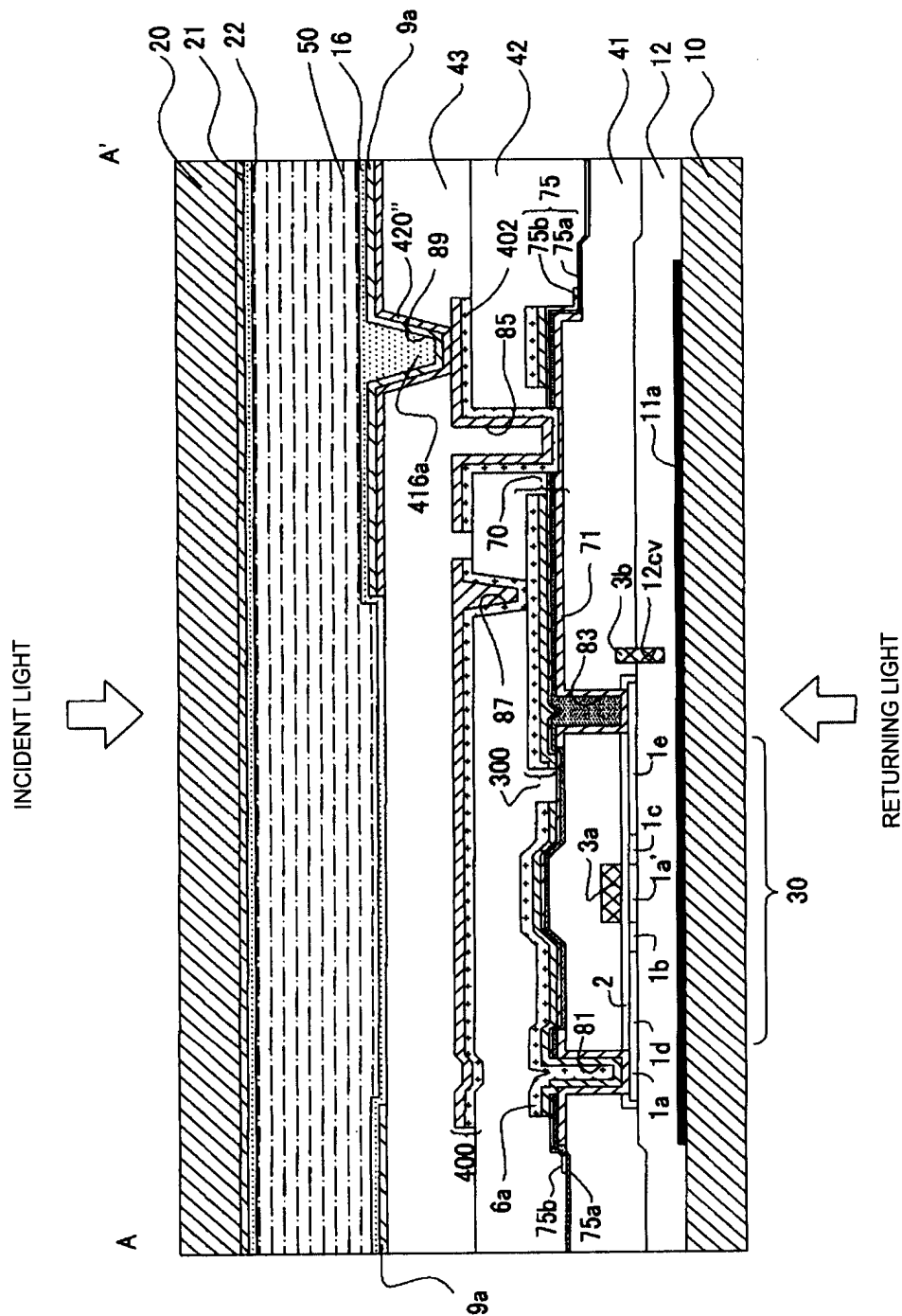
[FIG. 6]



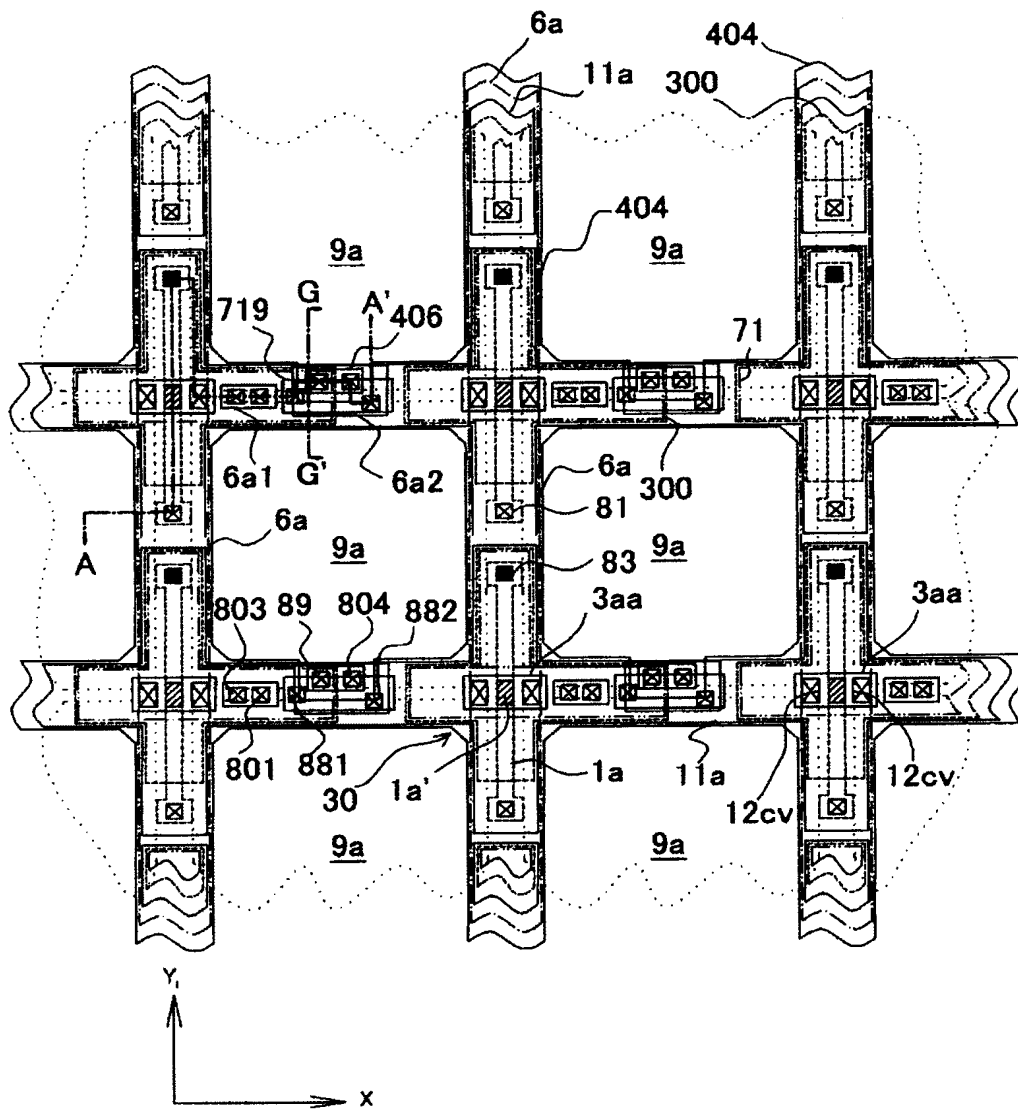
[FIG. 7]



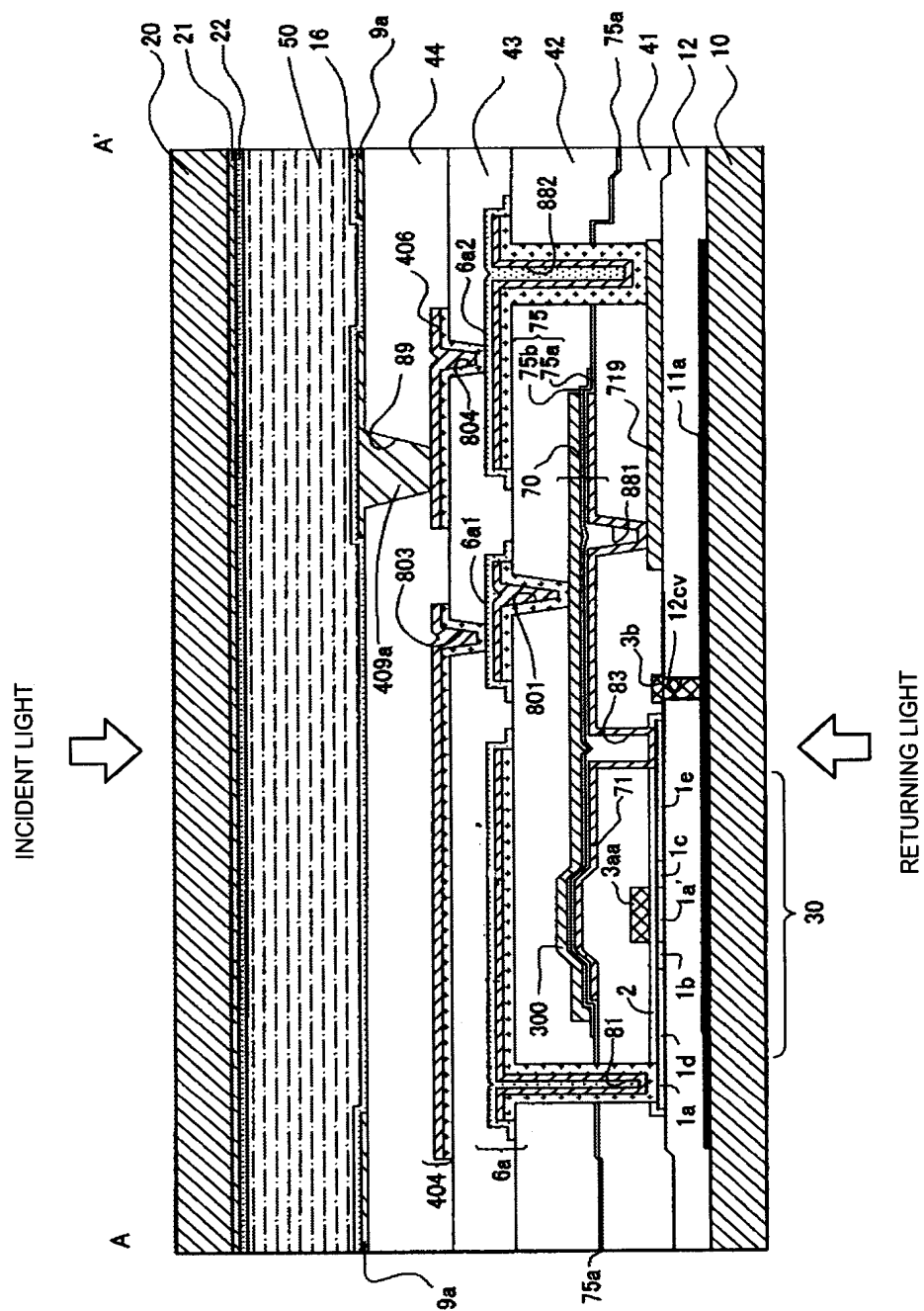
[FIG. 9]



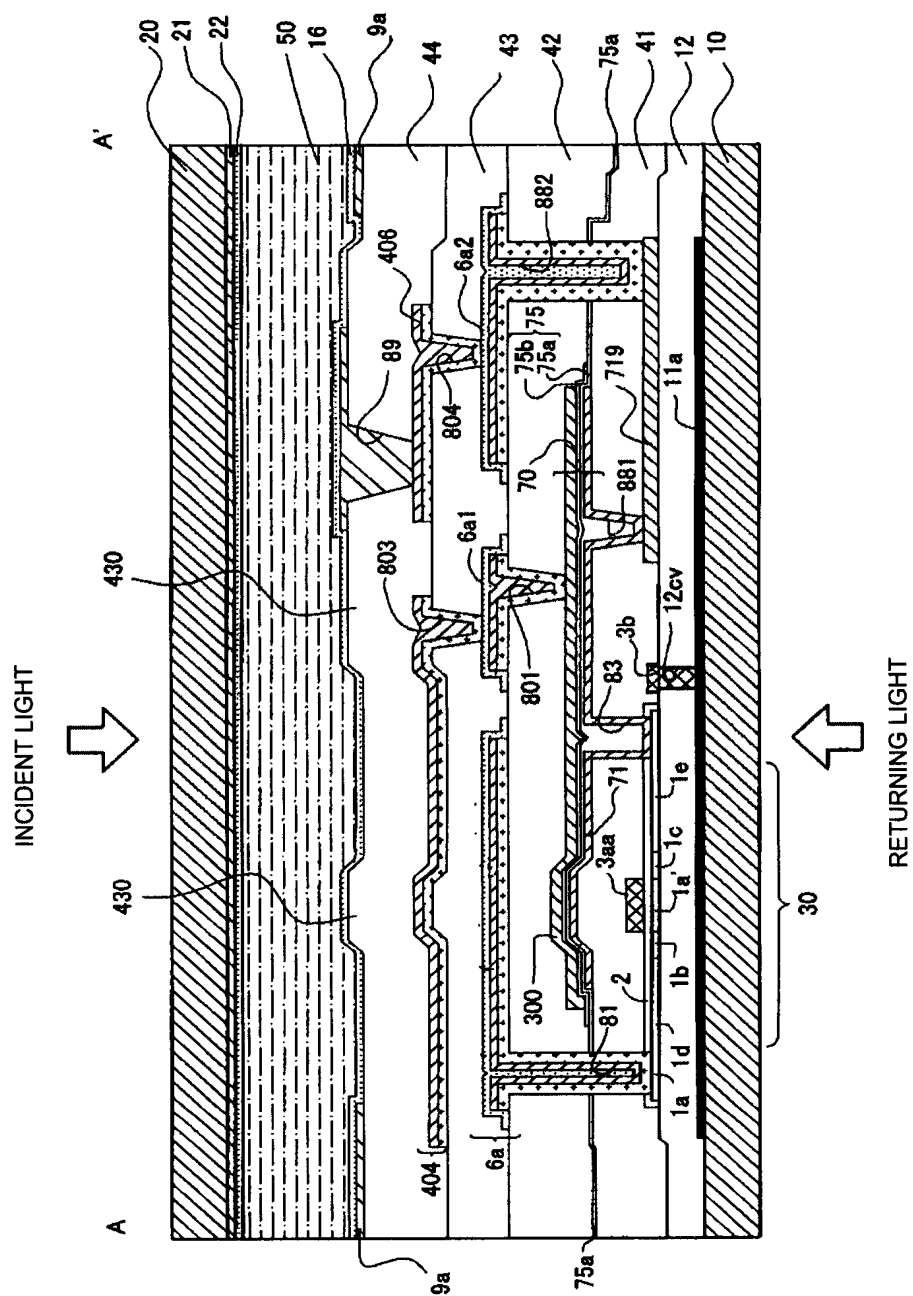
[FIG. 10]



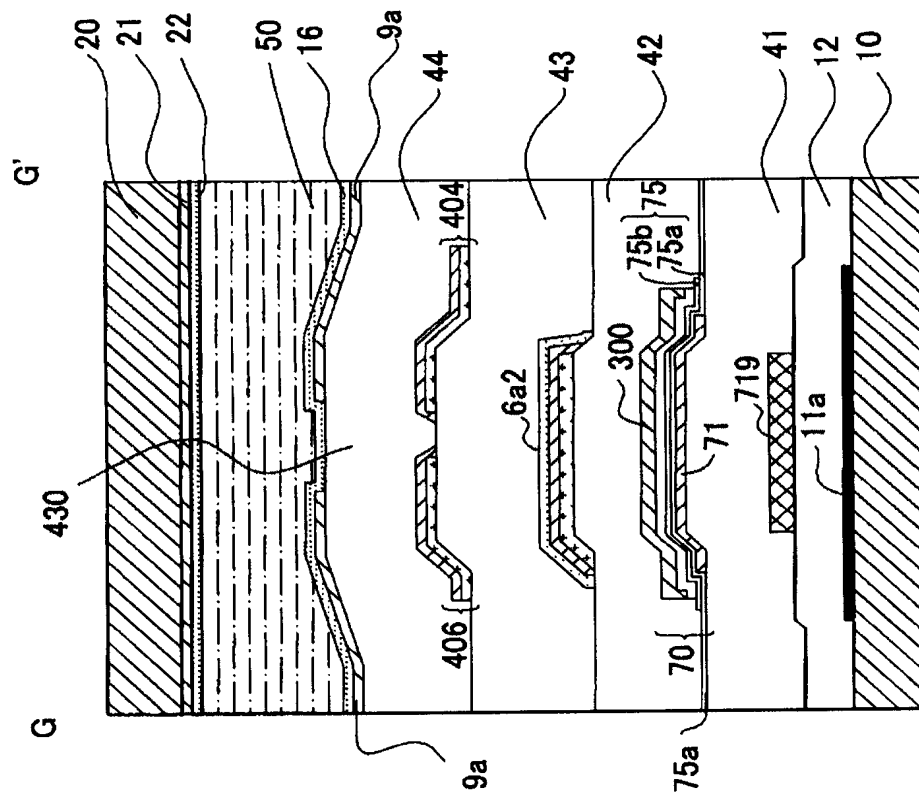
[FIG. 11]



[FIG. 12]

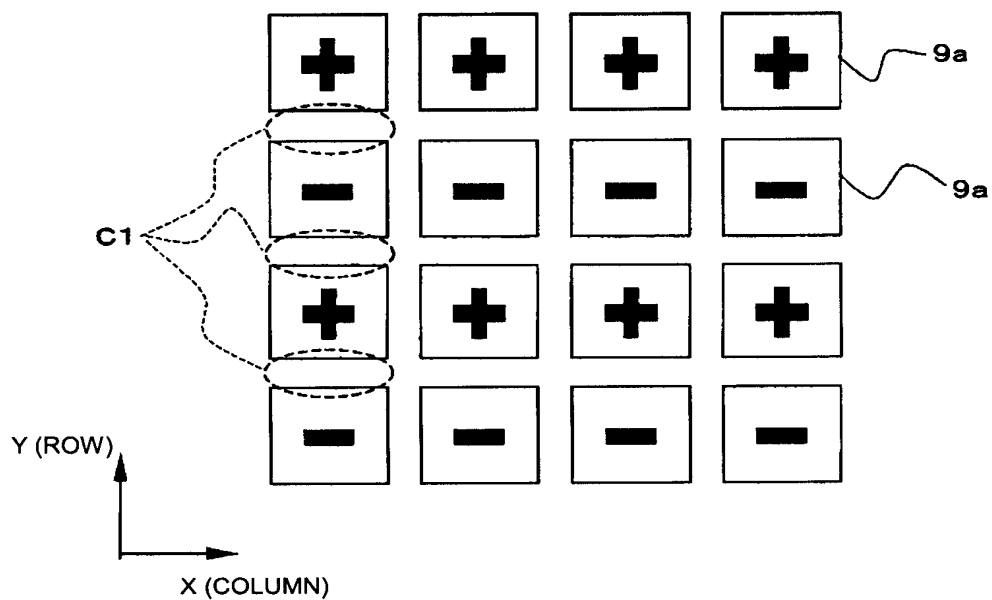


[FIG. 13]

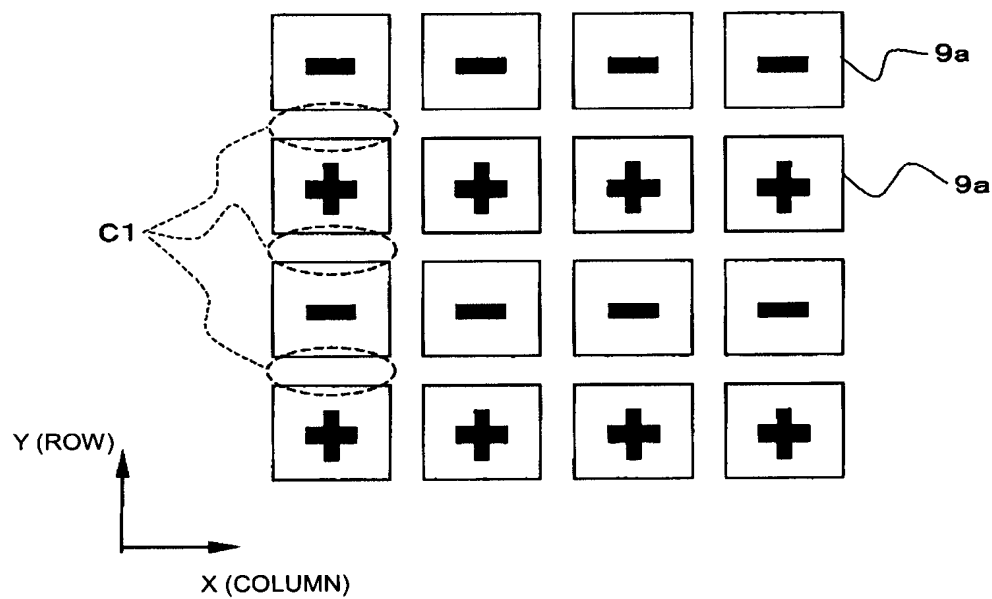


[FIG. 14]

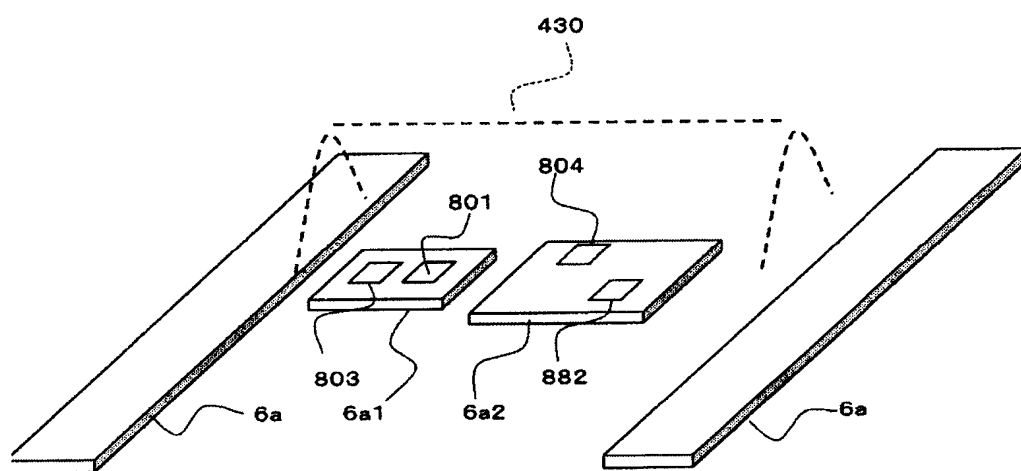
(a)



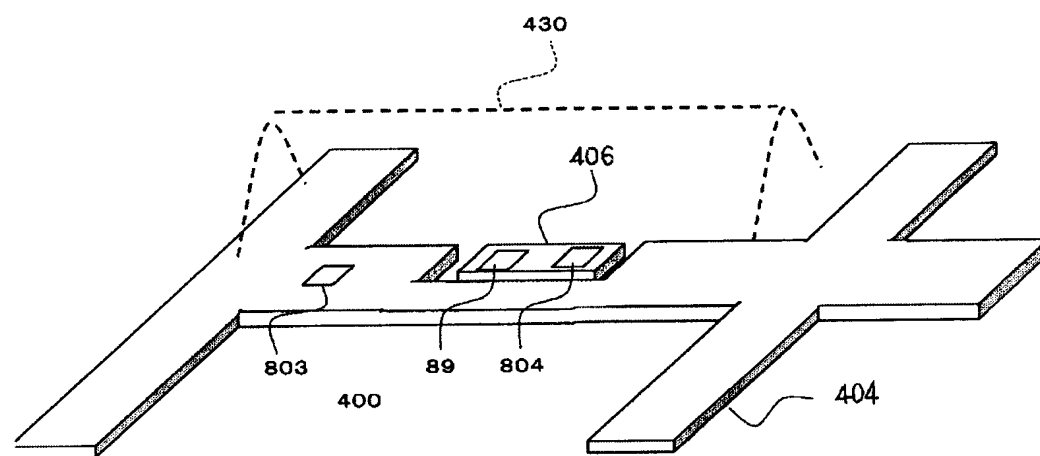
(b)



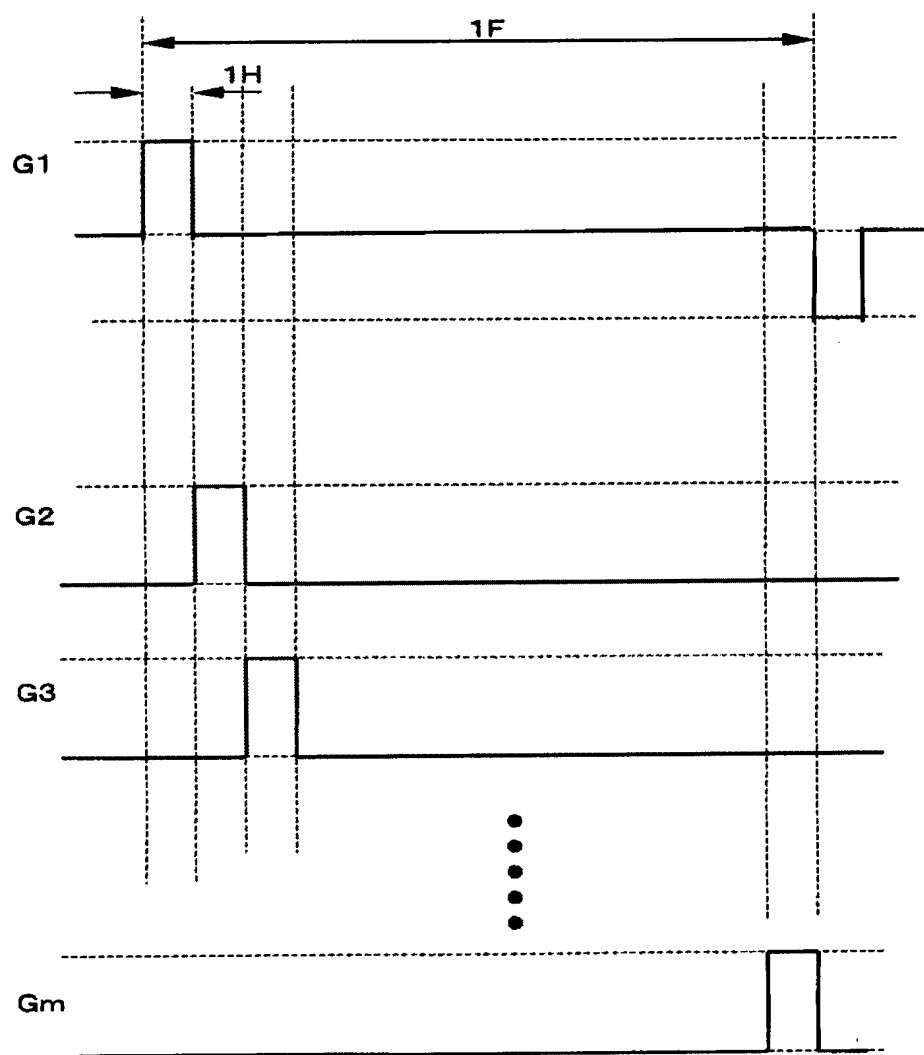
[FIG. 15]



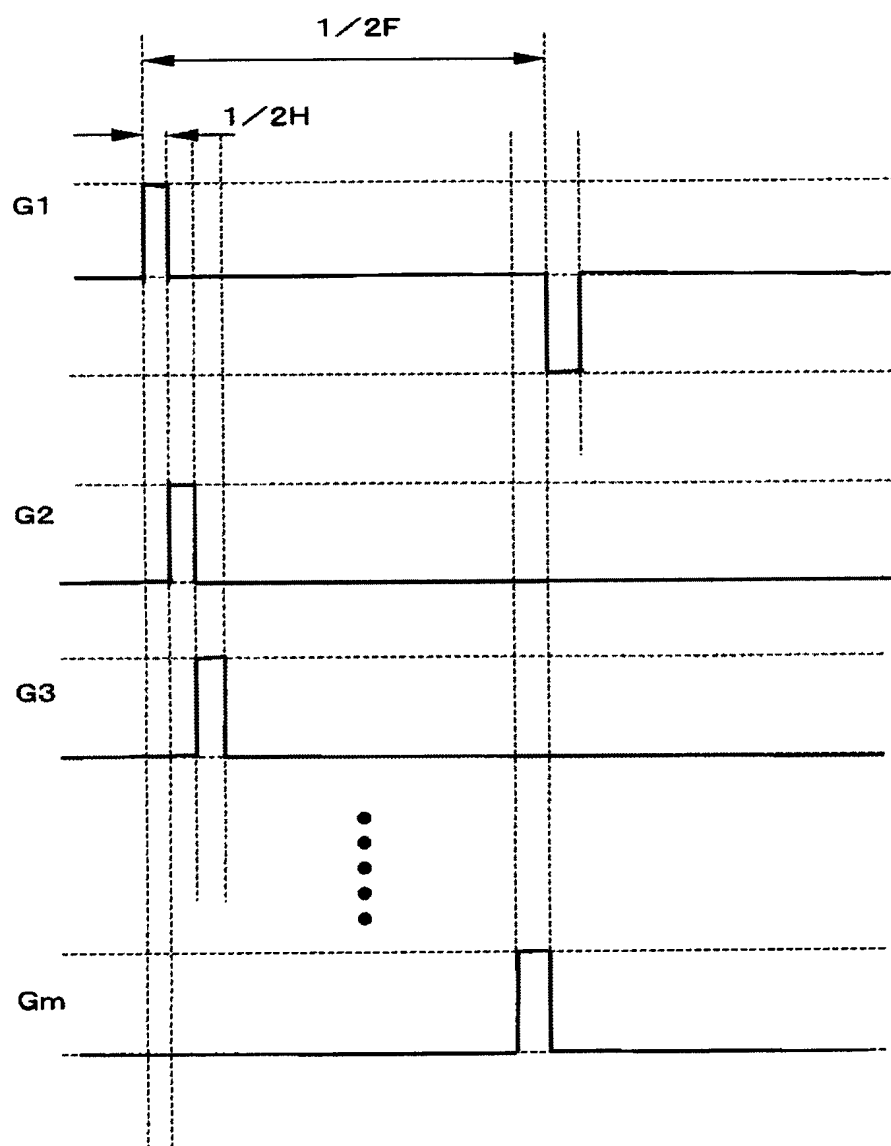
[FIG. 16]



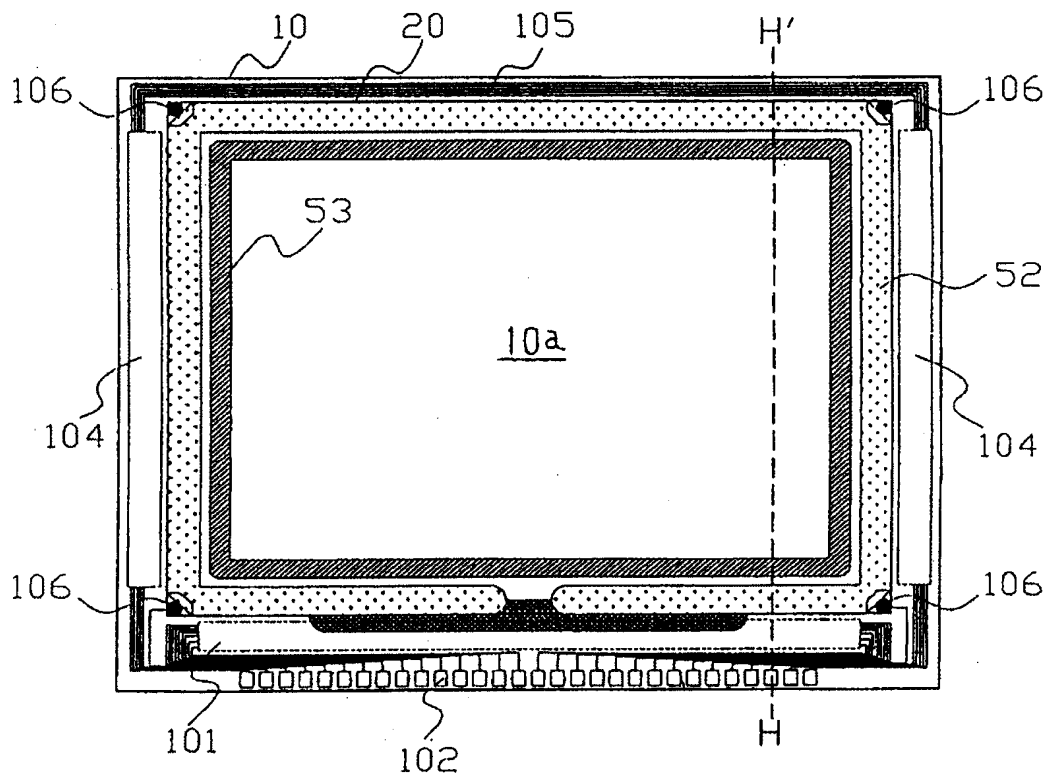
[FIG. 17]



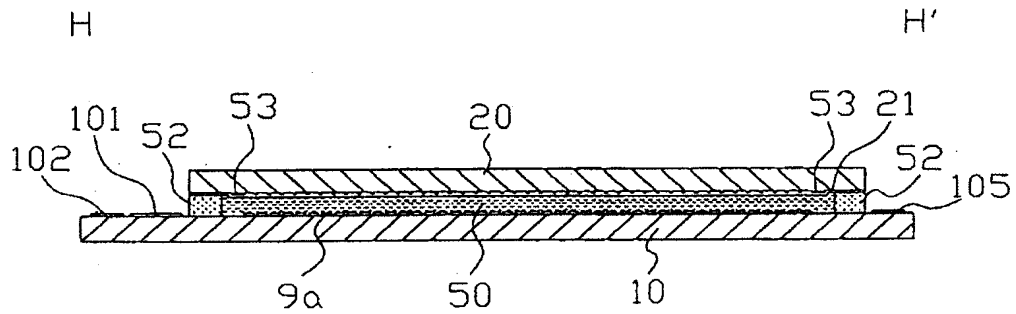
[FIG. 18]



[FIG. 19]



[FIG. 20]



[FIG. 21]

